

Data sheet mp6 electronics



Summary

This document contains detailed information on all electronic by Bartels Mikrotechnik for the mp6-micropump series.

Bartels electronics comprises of laboratory solutions with access to the full range of driving parameters down to smaller solutions for smart micro controller applications.

The evaluation board allows quick and flexible setups with our three integrable pumpdriver options with various flow and pressure sensors. Furthermore, we make an easy and user-friendly access possible with user-interfaces and Open-Source Codes for easier tinkering and prototyping for our customers.



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2 Revision History

Changes from Original (Jan. 2022) to Revision 1.1

- Important Note in 3.8.4 Examples of circuiting the mp-valvedriver added (p. 48)
- New chapter: 3.1.1 Power consumption for Highdriver platforms: mp-Highdriver/-Highdriver4 (p. 6)
- New chapter: 3.4.6 Memory Map for the mp-Lowdriver (p. 31)
- New Product name change: "mp-valve driver" to "mp-valvedriverT"
- New Product: Mp-Multiboard2 (p. 41)
- New Product: mp-valvedriverM (p. 47)
- New Product: mp-Driver (p. 36)

Changes from Revision 1.1 to 1.2

- Moved chapeters about USB driver installation, mp-Multiboard App user interface & further software related segments to separate datasheet
- New Chapter: 4. Electronics Examination
- New Chapter: 5. Discontinued Products
- Moved mp-Multiboard to chapter 4. Discontinued Products



3 Electronic drivers for mp6 series: Highdriver and Lowdriver platforms

3.1 Highdriver platforms: mp-Highdriver/-Highdriver4

The mp-Highdriver is a small, easy to use driving circuit developed for the micropumps of the mp6-series especially the mp-Highdriver4 as it can drive up to four mp6 pumps simultaneously. It generates amplitudes up to 250 Vpp from a 3-5 V DC supply.

Its low power consumption makes it ideal for battery powered handheld devices or even solar powered devices. The build-in interface allows the user to adapt frequency, amplitude and signal-shape to its application by the use of a few additional components or a microcontroller.

In order to locate Pin 1, please refer to the following figure. The pin is marked with a colored spot or triangular marking on the corner of the PCB.



Picture 1 mp-Highdriver and mp-Highdriver4

The Highdriver platform offers a high-voltage DC-AC converter that drives mp6 pumps. The device features a 2.7 V to 5.5 V input range that allows the device to accept a variety of sources such as single-cell Li+ batteries. The outputs of the device generate up to 250 VPP for maximum pump performance. The Highdriver platform utilizes a high-frequency spread-spectrum boost converter that reduces the amount of EMI/EFI generated by the circuit. The boost-converter switching frequency is set with an 8-bit register through the I2C interface. The mp-Highdriver4 uses a high-voltage full-bridge output stage to convert the high voltage generated by the boost converter to an AC waveform suitable for driving a piezo membrane pump. An internal register controlled through the I2C interface sets the shape of the output waveform. The output switching frequency for all outputs is set with an 8-bit register through the I2C interface. The Highdriver platform provides a serial digital interface that allows the user to set the peak voltage of their outputs independently with 5 bits of resolution. Highdriver platform) also provides an adjustable automatic ramping feature that slowly increases or decreases the peak output voltage when the set value is changed. The slew rate of the ramp is set with 3 bits of resolution through the I2C interface and it is independent for each channel. The high-voltage outputs are ESD protected up to ±15 kV Human Body Model, ±8 kV Air Gap Discharge, and ±6 kV Contact Discharge, as specified in IEC 61000-4-2.

3.1.1 Power consumption

The Highdriver platform can efficiently drive up to four pumps and is ideal for battery and applications. The power consumption is dependent of the pump frequency and the number of pumps that are active. Figure 1 shows the measured power consumption over the entire working area of the Highdriver.



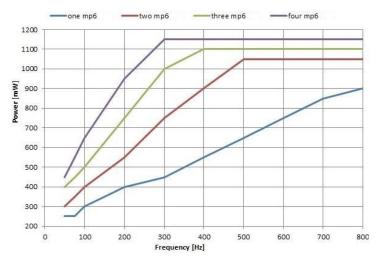


Figure 1 Power consumption of the Highdriverplatform at max amplitude 250Vpp over the pump frequency for varried no. of driven pumps at 5VDD

The power consumptions behaves linearly to the change of frequency. If more than one pump is active it reaches a max value at a certain point. Increasing the frequency any further comes with a decrease of the maximum amplitude.

3.1.2 Output Voltage

The shape, slope, frequency, ramp-on/-off times, and peak-to-peak voltage of the Highdriver platform lamp outputs are programmed using internal registers. The Highdriver platform is capable of producing output waveforms with varying shapes and slew rates. The user sets the shape and slew rate of the output using bits in the shape registers. The Highdriver platform output frequency uses an internal oscillator to set the desired frequency. The output frequency is adjusted by the FO[7:0] bits of the output frequency register. The frequency increases and decreases linearly with FO[7:0]. The peak-to-peak voltage of the output is varied from zero to 250 VPP by programming the VO_ _[4:0] bits of the ramping time and output peak voltage registers. The peak-to-peak voltage increases and decreases linearly with VO_ _[4:0]. The Highdriver platform also features a slow fade-on and slow fade-off time feature, it is programmed by the RT_ _ [2:0] bits of the ramping time and output peak voltage registers. This slow fade-on/-off feature causes the peak-to-peak voltage of the outputs to rise slowly from the previously set value to the maximum set value. This feature also causes the peak-to-peak voltage of the outputs to fall from the maximum set value to zero when the device is placed into shutdown. The slow rise and fall of the peak-to-peak output voltage creates a soft fade-on and fade-off of the mp6 pumps.

3.1.3 Boost Converter

The Highdriver platform boost converter consists of an external-tapped inductor from VDD to the LX input, an internal DMOS switch, an external diode from the secondary of the tapped inductor to the CS output, an external capacitor from the CS output to GND, and a mp6 pump connected to each output. When the DMOS switch is turned on, LX is connected to GND, and the inductor is charged. When the DMOS switch is turned off, the energy stored in the inductor is transferred to the capacitor CCS and the mp6. Note: The Highdriver platform exhibits high-voltage spikes on the LX node. The addition of a snubber circuit to the LX node protects the device by suppressing the high voltage spikes. The values of RSN and CSN should be optimized for the specific tapped inductor used. Typical values are RSN = 20Ω and CSN = 330pF. The Highdriver platform boost-converter frequency uses an internal oscillator to set the frequency of the boost converter. The oscillator frequency is adjusted by the FSW[4:0] bits of the boost-converter frequency register. The boost converter increases and decreases linearly with FSW[3:0]. To further reduce the amount of EMI/EFI generated by the circuit, the boost-converter frequency can be modulated (see the SS[1:0] bits of the boost-converter frequency register). Enabling modulation spreads the switching energy of the oscillator in the frequency domain, thus decreasing EMI.



3.1.4 Independent Dimming Control

The performance of the mp6-series pumps is proportional to the peak-to-peak voltage applied. The Highdriver platform provides four registers to control the peak-to-peak voltage of each output using the VO_ _[4:0] bits of the ramping time and output peak voltage registers.

3.1.5 Signal Output Waveshape

The Highdriver platform can produce sine-wave to square-wave waveshapes on the outputs by varying the slope of the outputs. This is achieved by using bits SL[1:0] of the shape register. If the shape configuration is set to sine and if all outputs have the same amplitude settings, then each output has a sinusoidal waveshape. If the outputs have different amplitude settings, then the output with the highest setting has a sine waveshape while the remaining outputs have a clamped sine waveshape.

3.1.6 Shutdown

The Highdriver platform can be placed in shutdown by writing a '0' to the EN bit of the system register. When activating shutdown, the pump outputs are shut down; however, the register contents remain unchanged.

3.1.7 Undervoltage Lockout (UVLO)

The Highdriver platform has a UVLO threshold of +2.0V (typ). When VDD falls below +2.0V (typ), the device enters a non-operative mode. The contents of the I2C registers are not guaranteed to remain unchanged below UVLO.

3.1.8 Thermal Protection

The Highdriver platform enters a non-operative mode if the internal die temperature of the device reaches or exceeds +160°C (typ). The mp-Highdriver4 is latched, and only cycling the power supply of the Highdriver platform resets the thermal protection bit as well as all registers.

3.1.9 I2C Registers and bit descriptions

Ten internal registers program the mp-Highdriver4. Table 1 lists all the registers, their addresses, and power-on reset states. All registers are read/write. Register 0x0A is reserved as a command to update all signal peak voltage output registers. Register 0x0B is reserved and should not be used.

Table	1	Register	map	mp-Highdriver

REGISTER	В7	B6	B5	B4	В3	B2	B1	ВО	REGISTER ADDRESS	POWER-ON RESET STATE			
SYSTEM													
Device Id	DEVID3	DEVID2	DEVID1	DEVIDO	REV3	REV2	REV1	REV0	0x00	0xB2			
Power Mode	OVR TEMP ¹	Χ	Χ	Χ	Χ	Χ	Χ	EN	0x01	0x00			
OUTPUT FREQUENCY													
Output Frequency	F07	F06	F05	F04	F03	F02	F01	F00	0x02	0x00			
Signal Shape													
Slope/Shape	Χ	ENDAM P	Χ	Χ	SHAPE 1	SHAPE 0	SL1	SL0	0x03	0x00			
BOOST-CONVER	BOOST-CONVERTER FREQUENCY												
Boost- Converter Frequency	SS1	SS0	X	FSW4	FSW3	FSW2	FSW1	FSW0	0x04	0x00			
AUDIO ²													



Audio Effects	FR_AM	NO_ SAMPL E	1	AUXDIV 0	AU4	AU3	AU2	AU1	0x05	0x00
SIGNAL RAMPIN	NG TIME A	AND PEAK	VOLIAGE							
Ramping Time and Peak Output Voltage CH1 ³	RT1 2	RT1_1	RT1_1	V01_4	V01_3	V01_2	V01_1	V01_0	0x06	0x00
Ramping Time and Peak Output Voltage CH2 ³	RT2 2	RT2_1	RT2_1	V02_4	V02_3	V02_2	V02_1	V02_0	0x07	0x00
Ramping Time and Peak Output Voltage CH3 ³	RT3 2	RT3_1	RT3_1	V03_4	V03_3	V03_2	V03_1	V03_0	0x08	0x00
Ramping Time and Peak Output Voltage CH4 ^{3,4}	RT4 2	RT4_1	RT4_1	V04_4	V04_3	V04_2	V04_1	V04_0	0x09	0x00

X = Don't Care

3.1.10 Slave address

The Highdriver platform device address is set through external inputs. The slave address consists of five fixed bits (B7–B3, set to 11110) followed by two input programmable bits (A1 and A0). For example: If A1 and A0 are hardwired to ground, then the complete address is 1111000. The full address is defined as the seven most significant bits followed by the read/write bit. Set the read/write bit to 1 to configure the mp-Highdriver4 to read mode. Set the read/write bit to 0 to configure the Highdriver platform to write mode. The address is the first byte of information sent to the mp-Highdriver4 after the START condition.

3.1.11 System Registers (0x00, 0x01)

Table 2 System Registers

Device

(DEVID3/DEVID2/DEVID1/DEVID0)	 Table 3.
Revision (REV3/REV2/REV1/REV0)	REV[3:0] is preprogrammed to the current revision of the mp-Highdriver4 and is $REV[3:0] = 0010$.
System Over temperature (OVRTEMP)	1 = Thermal shutdown temperature exceeded. 0 = Analog circuitry operating properly. OVETEMB = 1 turns the outputs off To set OVETEMB to 0 and restort in

OVRTEMP = 1 turns the outputs off. To set OVRTEMP to 0 and restart in default condition (all register reset), cycle the power supply of the mp-Highdriver4.

ID DEVID[3:0] is preprogrammed to 1011 to identify the mp-Highdriver4: see

...9....

Та	able 3 Device identification,	status and enable							
	REGISTER	B7	B6	B5	B4	В3	B2	B1	ВО
	0x00	DEVID3	DEVID2	DEVID1	DEVID0	REV3	REV2	REV1	REV0
	0x01	OVRTEMP	Χ	Χ	Χ	Χ	Χ	Χ	EN

¹Read back only

 $^{^{2}}$ Audio functions are not supported, as the audio input pin is not available. Keep the register value at the default (0x00)

³Send command OAh (update all signal ramping time and peak voltage registers) to have the programmed voltage effectively applied to the pumps 40nly this channel output is used for mp-Highdriver.



X = Don't Care

3.1.12 Output Frequency Register (0x02)

F0[7:6] sets the output frequency range of all output channels and F0[5:0] sets the output frequency within the frequency range; see Table 4. F0[5:0] = 000000 sets the frequency to the minimum value of the frequency range. F0[5:0] = 111111 sets the frequency to the maximum value of the frequency range. Output frequency increases linearly with F0[5:0]; see Table 5.

Table 4 Signal output frequency

REGISTER	В7	В6	B5	B4	В3	B2	B1	ВО
0x02	F07	F06	F05	F04	F03	F02	F01	F00

Table 5 Signal Frequency Range

FO[7:6]	FREQUENCY RANGE (Hz)
00	50-100
01	100-200
10	200-400
11	400-800

3.1.13 Output Shape Register (0x03)

Table 6 Output shape register

Damping Enable (ENDAMP) 1 = Active damping on LX node enabled.

0 = Active damping on LX node disabled.

ENDAMP = 1 actively damps the oscillation on the LX pin and could reduce EMI.

Shape (SHAPE1/SHAPE0) SHAPE[1:0] sets the desired output waveform; see Table 7 and Table 8.

Slew Rate (SL1/SL0) SL[1:0] sets the slope of the output; see Table 9.

Table 7 Signal shape configuration

REGISTER	В7	В6	B5	B4	В3	B2	B1	ВО
0x03	Χ	ENDAMP	Χ	Χ	SHAPE1	SHAPE0	SL1	SL0

X = Don't Care

Table 8: Signal Output Shape Configuration

SHAPE[1:0]	OUTPUT SHAPE
0X	Full Wave
10	Half Wave
11	Half Wave

X = Don't Care

Table 9: Signal Slope Configuration

SL[1:0]	OUTPUT SLOPE
00	Sine
01	Fast Slope
10	Faster Slope
11	Fastest Slope
11	(Square Wave)

3.1.14 Boost-Converter Frequency Register (0x04)

Table 10 Boost-converter frequency register

Spread Spectrum	SS[1:0] sets the spread-spectrum modulation frequency to a fraction of the boost-converter
(SS1/SS0)	frequency; see Table 11 and Table 12.



Boost-Converter Switching Frequency (FSW[4:0]) FSW4 sets the switching frequency range of the boost converter and FSW[3:0] sets the switching frequency within the frequency range; see Table 13. The frequency range for FSW4 = 0 is 800 kHz - 1600 kHz. The frequency range for FSW4 = 1 is 400 kHz-800kHz. FSW[3:0] = 0000 sets the frequency to the minimum value of the frequency range. FSW[3:0] = 1111 sets the frequency to the maximum value of the frequency range. Boost-converter switching frequency increases linearly with FSW[3:0].

Table 11: Boost-Converter Configurations

REGISTER	В7	В6	B 5	B4	В3	B2	B1	ВО
0x04	SS1	SS0	Χ	FSW4	FSW3	FSW2	FSW1	FSW0

Table 12: Spread-Spectrum Configuration

SS[1:0]	SPREAD-SPECTRUM
00	Disabled
01	1/8
10	1/32
11	1/128

Table 13: Boost-Converter Frequency Range

FSW3	FSW2	FSW1	FSW0	BOOST-CONVERTER SWIT FSW4 = 0	CHING FREQUENCY (kHz) FSW4 = 1
0	0	0	0	800	400
0	0	0	1	853	427
0	0	1	0	907	453
0	0	1	1	960	480
0	1	0	0	1013	507
0	1	0	1	1067	533
0	1	1	0	1120	560
0	1	1	1	1173	587
1	0	0	0	1227	613
1	0	0	1	1280	640
1	0	1	0	1333	667
1	0	1	1	1387	693
1	1	0	0	1440	720
1	1	0	1	1493	747
1	1	1	0	1547	773
1	1	1	1	1600	800

3.1.15 Ramping Time and Peak Output Voltage Register (0x06, 0x07, 0x08, 0x09)

Table 14 Ramping time and peak output voltage register

Ramping Time $RT_{2:0}$ sets the ramp time of each output; see Table 16. $(RT4_{RT3}_{RT3}_{RT2}_{RT1})$

Output Peak-to-Peak Voltage (VO1_ _/VO2_ _/VO3_ _/VO4_ _)

VO_ _[4:0] controls the peak-to-peak voltage of each output. When VO_ _[4:0] = 00000, the output follows COM. When VO_ _[4:0] = 11111, the output has a 250V peak with respect to COM. The output voltage rises linearly with VO_ _[4:0].

Table 15: Output Configuration

REGISTER	В7	В6	B5	B4	В3	B2	B1	ВО
0x06	RT1_2	RT1_1	RT1_1	V01_4	V01_3	V01_2	V01_1	V01_0
0x07	RT2_2	RT2_1	RT2_1	V02_4	V02_3	V02_2	V02_1	V02_0
0x08	RT3_2	RT3_1	RT3_1	V03_4	V03_3	V03_2	V03_1	V03_0
0x09	RT4_2	RT4_1	RT4_1	V04_4	V04_3	V04_2	VO4_1	V04_0



Table 16: Ramping Time Configuration

RT[2:0]	Ramping Time (ms)
000	<0.1
001	62.5
010	125
011	250
100	500
101	750
110	1000
111	2000

3.1.16 I²C Interface

The Highdriver platform features an I^2C -compatible as a slave device, 2-wire serial interface consisting of a serial data line (SDA) and a serial-clock line (SCL). SDA and SCL facilitate communication to the device at clock rates up to 400 kHz. Figure $_{10}$ shows the 2-wire interface-timing diagram. The master generates SCL and initiates data transfer on the bus. A master device writes data to the mp-Highdriver4 by transmitting the proper slave address followed by the register address and then the data word. Each transmit sequence is framed by a START (S) or REPEATED START (Sr) condition and a STOP (P) condition. Each word transmitted to the mp-Highdriver4 is 8 bits long and is followed by an acknowledge clock pulse. A master reading data from the mp-Highdriver4 transmits data on SDA in sync with the master-generated SCL pulses. The master acknowledges receipt of each byte of data. Each read sequence is framed by a START or REPEATED START condition, a not acknowledge, and a STOP condition. SDA operates as both an input and an open-drain output. A pull-up resistor, typically greater than 500Ω , is required on SCL if there are multiple masters on the bus, or if the master in a single-master system has an open-drain SCL output. Series resistors in line with SDA and SCL are optional. Series resistors protect the digital inputs of the mp-Highdriver4 from high-voltage spikes on the bus lines, and minimize crosstalk and undershoot of the bus signals.

3.1.17 Bit Transfer

One data bit is transferred during each SCL cycle. The data on SDA must remain stable during the high period of the SCL pulse. Changes in SDA while SCL is high are control signals (see the START and STOP Conditions section). SDA and SCL idle high when the I2C bus is not busy.

3.1.18 START and STOP Conditions

SDA and SCL idle high when the bus is not in use. A master initiates communication by issuing a START condition. A START condition is a high-to-low transition on SDA with SCL high. A STOP condition is a low-to-high transition on SDA while SCL is high (Figure 2). A START condition from the master signals the beginning of a transmission to the mp-Highdriver4. The master terminates transmission and frees the bus by issuing a STOP condition. The bus remains active if a REPEATED START condition is generated instead of a STOP condition.

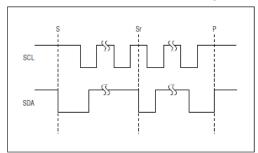


Figure 2: START, STOP and REPEATED START Conditions



3.1.19 Early STOP Conditions

The Highdriver platform recognizes a STOP condition at any point during data transmission, except if the STOP condition occurs in the same high pulse as a START condition. For proper operation, do not send a STOP condition during the same SCL high pulse as the START condition.

3.1.20 Acknowledge

The acknowledge bit (ACK) is a clocked 9.th bit that the mp-Highdriver4 uses to handshake receipt each byte of data when in write mode (see Figure 3). The Highdriver platform pulls down SDA during the entire master generated 9th clock pulse if the previous byte is successfully received. Monitoring ACK allows for detection of unsuccessful data transfers. An unsuccessful data transfer occurs if a receiving device is busy or if a system fault had occurred. In the event of an unsuccessful data transfer, the bus master may retry communication. The master pulls down SDA during the 9.th clock cycle to acknowledge receipt of data when the Highdriver platform is in read mode. An acknowledge is sent by the master after each read byte to allow data transfer to continue. A not-acknowledge is sent when the master reads the final byte of data from the Highdriver platform followed by a STOP condition.

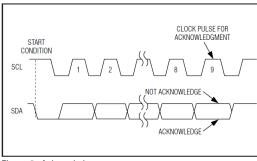
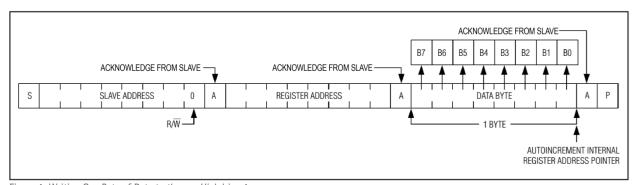


Figure 3: Acknowledge

3.1.21 Write Data Format

A write to the Highdriver platform includes transmission of a START condition, the slave address with the R/W bit set to 0, one byte of data to configure the internal register address pointer, one or more bytes of data, and a STOP condition. Figure 4 illustrates the proper frame format for writing one byte of data to the Highdriver platform. Figure 5 illustrates the frame format for writing n-bytes of data to the Highdriver platform. The slave address with the R/W bit set to 0 indicates that the master intends to write data to the Highdriver platform. The Highdriver platform acknowledges receipt of the address byte during the master-generated 9.th SCL pulse. The second byte transmitted from the master configures the mp-Highdriver4 internal register address pointer. The pointer tells the mp-Highdriver4 where to write the next byte of data. An acknowledge pulse is sent by the Highdriver platform upon receipt of the address pointer data. The third byte sent to the Highdriver platform contains the data that will be written to the chosen register. An acknowledge pulse from the Highdriver platform signals receipt of the data byte. The address pointer automatically increments to the next register address after each received data byte. This auto increment feature allows a master to write to sequential registers within one continuous frame. Attempting to write to register addresses higher than 0x0B results in repeated writes of 0x0B. Figure 5 illustrates how to write to multiple registers with one frame. The master signals the end of transmission by issuing a STOP condition.







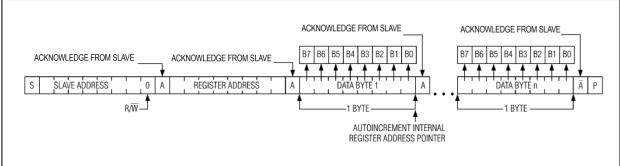


Figure 5: Writing n-Bytes of Data to the mp-Highdriver4

3.1.22 Read Data Format

Send the slave address with the R/W set to 1 to initiate a read operation. The Highdriver platform acknowledges receipt of its slave address by pulling SDA low during the 9th SCL clock pulse. A START command followed by a read command resets the address pointer to register 0x00. The first byte transmitted from the Highdriver platform will be the contents of register 0x00. Transmitted data is valid on the rising edge of the master-generated serial clock (SCL). The address pointer automatically increments after each read data byte. This auto increment feature allows all registers to be read sequentially within one continuous frame. A STOP condition can be issued after any number of read data bytes. If a STOP condition is issued followed by another read operation, the first data byte to be read will be from register 0x00 and subsequent reads will auto increment the address pointer until the next STOP condition. The address pointer can be preset to a specific register before a read command is issued. The master presets the address pointer by first sending the Highdriver platform's slave address with the R/W bit set to 0 followed by the register address. A REPEATED START condition is then sent, followed by the slave address with the R/W set to 1. The Highdriver platform transmits the contents of the specified register. The address pointer automatically increments after transmitting the first byte. Attempting to read from register addresses higher than 0x0B results in repeated reads of 0x0B. The master acknowledges receipt of each read byte during the acknowledge clock pulse. The master must acknowledge all correctly received bytes except the last byte. The final byte must be followed by a not acknowledge from the master and then a STOP condition. Figure 6 illustrates the frame format for reading one byte from the mp-Highdriver4. Figure 7 illustrates the frame format for reading multiple bytes from the Highdriver platform.

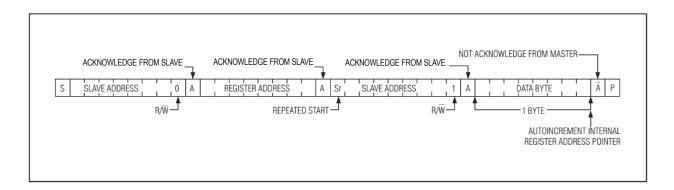




Figure 6: Reading One Indexed Byte of Data from the mp-Highdriver4

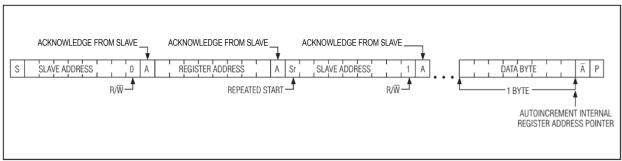


Figure 7: Reading n-Bytes of Indexed Data from the mp-Highdriver4

3.1.23 Signal Shapes

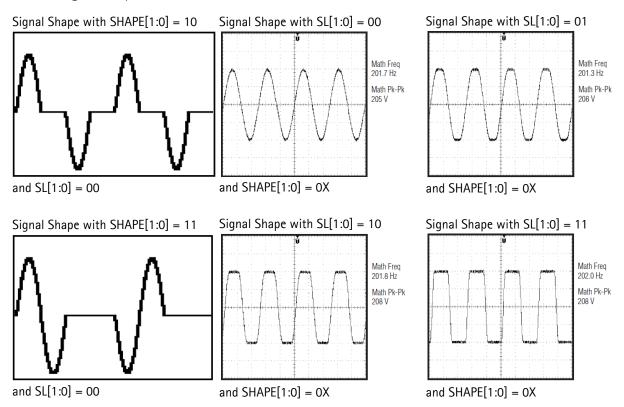


Figure 8: Signal shapes



3.2 mp-Highdriver Specifics

3.2.1 Pinout Highdriver

Pin	Name	Function				
1,2,17,18	NC	This pins should not be connected and left floating				
3	GND	Ground				
		Predefined clock signal. Frequency will be set to the				
		nominal 200 Hz, when this pin is connected to CLK_INT				
4	CLK	(see image "Schematic 1" in chapter 3.2.4).				
4	CLIX	It is also possible to connect an external clock signal with				
		a <u>quadruplicated</u> frequency of the micropumps		IC1		
		frequency.	_1_	NC NC	NC	18
5	CLK_INT	When connected to CLOCK the frequency is set to 200 Hz.				, -
6	AMP	The amplitude can be set with an analogue voltage		NC NC	NC	17
0	Alvii	between 0.5 V to 1.3 V.	_3_	GND	VDD	16
		Address Input O. Address inputs allow up to four			7.00	
7	A0	connections on one common bus. Connect A0 to GND or	4	CLK	/RESET	15
		VDD.	5	- CLK_INT +	SDA	14
8	P1+	Piezo actuator 1, positive electrode		CLK_INI +	2DH	
9	P1-	Piezo actuator 1, negative electrode	_6_	AMP	SCL	13
10	P2-	Piezo actuator 2, negative electrode	7			12
11	P2+	Piezo actuator 2, positive electrode		AØ	A1	
		Address Input 1. Address inputs allow up to four	_8_	P1+	P2+	11
12	A1	connections on one common bus. Connect A1 to GND or				1,0
		VDD.	9_	P1-	P2-	10
13	SCL	Serial-Clock Input. SCL requires an external pullup		MP6-X0EM		
13	JCL	resistor.				
14	SDA	Open-Drain, Serial Data Input/Output. SDA requires an				
17	JUA	external pullup resistor.				
		If the I2C-Bus is to be used, the internal processor has to				
15	/RESET	be disabled by applying GND to this pin. Otherwise				
		connect this pin to VDD for normal operation.				
16	VDD	Input Supply voltage				



3.2.2 Technical specifications mp-Highdriver

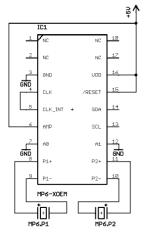
mp-Highdriver controller	Order code: mp-Highdriver
The mp-Highdriver controller drives th	e micropump at adjustable performance in a package similar to an
integrated circuit. It enables integration	into system electronics or on a PCB.
Dimensions	10,16 x 25,40 x 2.82 mm
	0.4 x 1.0 x 0.11 in
Adjustable parameters	amplitude, frequency, wave form
Amplitude range	10 – 250 Vpp
Frequency range	50 - 800 Hz
Signal form	sine, rectangular, trapezoid
Power supply	2.7 – 5.5 VDC
Pin arrangement	DIL 18; horizontal 2.54 mm, vertical 7.62 mm

3.2.3 Electrical Characteristics mp-Highdriver

Parameter	Symbol	Conditions	Min	Тур.	Max	Unit
Supply voltage	VDD		2.7		5.5	V
Average current consumption	IDD	VDD = 5 V		40		mA
Setting range AMPLITUDE			0.5		1.3	VDC
min. voltage at pump	Vpump	AMPLITUDE = 0.5 VDC		100		Vpp
max. voltage at pump (1)	Vpump	AMPLITUDE = 1.25 VDC	250	260	270	Vpp
Frequency output	F	VDD = 5 V (Default)		200		Hz
Digital Low-Signal					0	V
Digital High-Signal			2			V
Input current AMPLITUDE			1		3	μΑ
Operating current during Shutdown Mode				1.6		μΑ

3.2.4 Ex. of circuiting the mp-Highdriver: Operation with fixed settings

The mp-Highdriver can operate the micropumps of the mp6-series without further external components. In this case, frequency and amplitude to the micropump are predefined to 250 Vpp and 200 Hz by the internal circuit.



Schematic 1 Predefined amplitude of 250 Vpp and frequency of 200 Hz by internal circuit



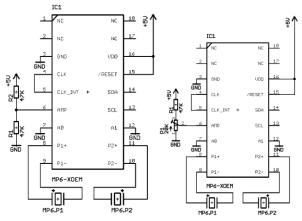
3.2.5 Ex. of circuiting the mp-Highdriver: Operation with variable settings & external components

In this example, amplitude to the micropump will be defined by external components. The amplitude can be varied in the range from 0 Vpp to 250 Vpp.

Using a voltage divider, it is possible to set the amplitude with only two additional resistors. See image Schematic 2 below for an example. First picture shows two resistors generating the reference voltage for the AMP-pin. The voltage at the AMP-pin can be calculated with this formula:

$$V_{AMPLITUDE} = V_{DD} \cdot \frac{R1}{R1 + R2}$$

As an alternative an appropriate potentiometer can be used to adjust the reference voltage. On the second picture a potentiometer and an offset resistor are used to generate the reference voltage for the AMP-pin.



Schematic 2 Defining amplitude with external components

The relation of the voltage at the AMP-pin vs. the output voltage is shown in the picture below. It shows a gain of approx. 200:1 between reference voltage and output voltage.

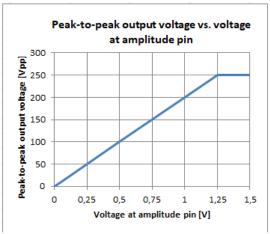


Figure 9 OEM output voltage changed with external components

Unlike the mp6-OEM, the frequency on the mp-Highdriver cannot be selected via external capacitor. This can only be done my supplying a clock signal to the CLK-pin witch is shown in the next chapter.

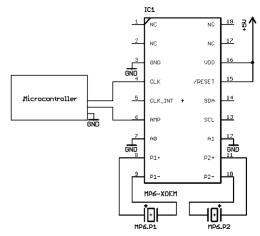
3.2.6 Ex. of circuiting the mp-Highdriver: Operation with variable settings via microcontroller

Using a microcontroller to operate the micropump, a square wave signal (OV-VDD; 50% duty-cycle) with four times the desired output frequency has to be supplied to the CLK Pin.

The amplitude can be set with an analog voltage between 0 V and 1.25 V. Voltages above 1.25V up to VDD are allowed and keep the output voltage at maximum.



For minimizing the power consumption, the mp-Highdriver can be switched off. To do this, apply a OV signal to the AMP-pin.

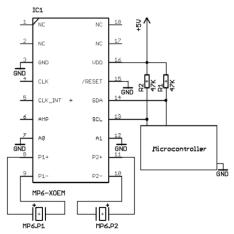


Schematic 3 Controlling settings via microcontroller

3.2.7 Ex. of circuiting the mp-Highdriver: Operation with variable settings via I²C-Interface

The mp-Highdriver can also be operated using the I^2C -Interface. Using this interface all the features of the chip can be accessed. The functionality and procedures are identical to the mp6-Highdriver4. A complete interface and protocol description can be found in the I^2C section of this document.

To activate the I²C-Interface the onboard processor has to be deactivated by tying the reset pin to Ground. Select a desired slave-address connecting pins A0 and A1 to either VDD or GND and interface the chip through the I²C-pins SDA and SCL. Both need an external pullup resistor (4,7K recommended) tied to VDD. (There are no pullup resistors onboard the mp-Highdriver)



Schematic 4 Controlling settings via I²C

3.3 mp-Highdriver4 Specifics

3.3.1 Pinout mp-Highdriver4

PIN	NAME	FUNCTION
1	GND	Ground
2	VDD	Input Supply Voltage
3	AO	Address Input O. Address inputs allow up to four connections on one common bus. Connect AO to GND or VDD.
4	A1	Address Input 1. Address inputs allow up to four connections on one common bus. Connect A1 to GND or VDD.



5	SDA	Open-Drain, Serial Data Input/Output. SDA requires an external pull-up resistor					
6	SCL	Serial-Clock Input. SCL requires an external pull-up resistor.					
7	COM¹ PIEZO31-	High-Voltage common Output. Connect to negative input of piezo 1 of pump 3					
8	PIEZO31+	High-Voltage Output 31+. Connect to positive input of piezo 1 of pump 3	1 2 3 4	GND VDD A0 A1		VDD GND NC NC	28 27 26 25
9	PIEZO32+	High-Voltage Output 32+. Connect to positive input of piezo 2 of pump 3	5	SDA SCL		NC NC	24 23
10	PIEZO32-	High-Voltage Output 32 Connect to negative input of piezo 2 of pump 3	8 9 10	PIEZO31- PIEZO31- PIEZO32- PIEZO32-	F	PIEZO22- PIEZO22+ PIEZO21- PIEZO21-	21 20 19
11	COM¹ PIEZO41-	High-Voltage common Output. Connect to negative input of piezo 1 of pump 4	12 13 14	PIEZO41- PIEZO42- PIEZO42-	F	PIEZO12+ PIEZO11+ PIEZO11-	17 16 15
12	PIEZO41+	High-Voltage Output 41+. Connect to positive input of piezo 1 of pump 4					J
13	PIEZO42+	High-Voltage Output 42+. Connect to positive input of piezo 2 of pump 4					
14	PIEZO42-	High-Voltage Output 42 Connect to negative input of piezo 2 of pump 4					
15	COM¹ PIEZO11-	High-Voltage common Output. Connect to negative input of piezo 1 of pump 1					
16	PIEZO11+	High-Voltage Output 11+. Connect to positive input of piezo 1 of pump 1					
17	PIEZO12+	High-Voltage Output 12+. Connect to positive input of piezo 2 of pump 1					
18	PIEZO12-	High-Voltage Output 12 Connect to negative input of piezo 2 of pump 1					
19	COM¹ PIEZO21-	High-Voltage common Output. Connect to negative input of piezo 1 of pump 2					
20	PIEZO21+	High-Voltage Output 21+. Connect to positive input of piezo 1 of pump 2					
21	PIEZO22+	High-Voltage Output 22+. Connect to positive input of piezo 2 of pump 2					
22	PIEZO22-	High-Voltage Output 22 Connect to negative input of piezo 2 of pump 2					
23	NC	Not connected (do not connect)					
24	NC	Not connected (do not connect)					
25	NC	Not connected (do not connect)					
26	NC	Not connected (do not connect)					
27	GND	Ground					
28	VDD	Input Supply Voltage					

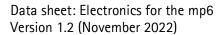


3.3.2 Electrical Characteristics mp-Highdriver4

Table 17 Absolute Maximum Ratings

VDD	-0.3V to +6.0V	A0, A1	-0.3V to +6.0V
PIEZOXX, COM	-0.3V to +160V	SCL, SDA	-0.3V to (VDD + 0.3V)

ble 18 Electrical Characteristics	CVMDOL	CONDITIONS	NAINI	TVD	NANY	UNITS
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	-
Input Voltage Input Supply Current	V _{DD}	All channels on, 300V _{PP} , f = 100Hz, sinewave output shape	2.7	75	5.5	MA mA
Shutdown Supply Current	I _{SHDN}	A0, A1 = 0V or V_{DD} ; SCL = SDA = GND or V_{DD} ; not toggling		25	100	nA
Undervoltage Lockout	VUV	V _{DD} rising	1.6	2.0	2.5	V
PIEZO OUTPUTS						
Peak-to-Peak Output		PIEZO+ - PIEZO; VO[4:0]=01000; V _{DD} =5.0V PIEZO+ - PIEZO;		75		
Voltage	V _{PP}	VO[4:0]=10000; V _{DD} =5.0V PIEZO+ - PIEZO;		150 250		V
	t	V0[4:0]=11111; V _{DD} =5.0V F0[7:0]=10000000; V _{DD} =5.0V	194		206	
Switching Frequency	f _{LR}	F0[7:0]=10000000; V _{DD} =5.0V F0[7:0]=10111111; V _{DD} =5.0V	388	200 400	206 412	- Hz
BOOST CONVERTER	THR	10[7.0]=10111111, VDD=3.0V	300	400	412	
Peak Output Voltage	V _{cs}	VO[4:0] = 01000; V _{DD} = 5.0V VO[4:0] = 10000; V _{DD} = 5.0V VO[4:0] = 11111; V _{DD} = 5.0V		40 75 150		V
Tapped-Inductor Center Switching Frequency	fsw	FSW[4:0] = 10000 FSW[4:0] = 11111 FSW[4:0] = 00000 (default) FSW[4:0] = 01111	800 800 1600			kHz
Tapped-Inductor Switching Frequency Spreading Factor	S _F	SS[1:0] = 01, 10, or 11	8			%
I ² C INTERFACE LOGIC (SDA, S	1	0)				
Input Logic-Low Voltage	V _{IL}				0.5	V
Input Logic-High Voltage	V _{IH}		1.5			V
Input Hysteresis	I _{HYS}			130		mV
Input Leakage Current	I _{LKG}		-1		+1	μΑ
Output Low Voltage	V _{OL}	ISINK = 3mA			0.4	V
Input/Output Capacitance	C _{I/O}		10			pF
Serial-Clock Frequency	f _{SCL}				400	kHz
Clock Low Period	t _{LOW}		1.3			μs
Clock High Period	t _{HIGH}		0.6			μs
Bus Free Time	t _{BUF}		1.3			μs
START Setup Time	t _{SU,STA}		0.6			μs
START Hold Time	t _{HD,STA}		0.6			μs
STOP Setup Time	t _{SU,STO}		0.6	1		μs
Data In Setup Time	t _{SU,DAT}		100			ns





Data In Hold Time	t _{HD,DAT}		0		900	ns
Receive SCL/SDA Minimum Rise Time	t _R			20 + 0.1C _B		ns
Receive SCL/SDA Maximum Rise Time	t _R			300		ns
Receive SCL/SDA Minimum Fall Time	t _F			20 + 0.1C _B		ns
Receive SCL/SDA Maximum Fall Time	t _F			300		Ns
Transmit SDA Fall Time	t _F	C _B = 400pF	20 + 0.1C _B		300	ns
SCL/SDA Noise Suppression Time	tı			50		ns
ESD PROTECTION					·	
		Human Body Model	<u>±</u> 15			
PIEZO+ to PIEZO		IEC 61000-4-2 Contact Discharge	<u>+</u> 6			kV
		IEC 61000-4-2 Air Gap Discharge	<u>+</u> 8			
THERMAL PROTECTION						
Thermal Shutdown	T _{SHDN}		160			°C
Thermal Shutdown Hysteresis	T _{HYST}		12			°C

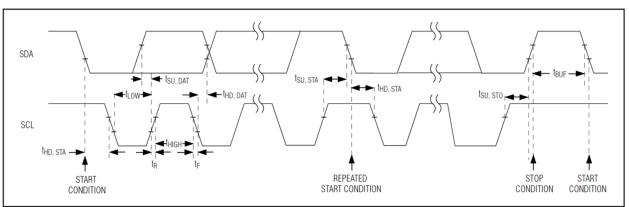
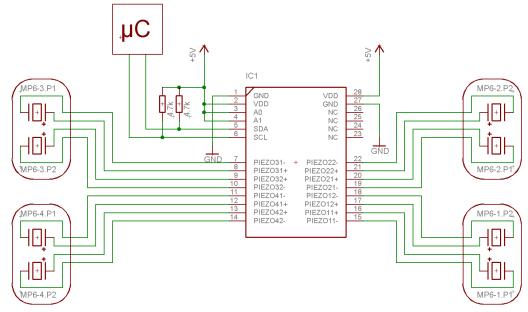


Figure 10: I2C Timing Specifications

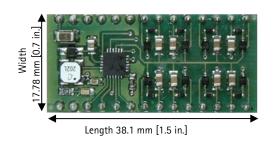


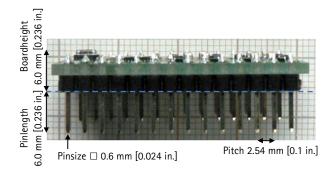
3.3.3 Examples of circuiting the mp-Highdriver4



Schematic 5 Typical Operating circuit for the mp-Highdriver4

3.3.4 Packaging Dimensions





All pins have a square cross section of 0.6 mm (0.024 in.)

3.3.5 Example code for mp-Highdriver

Wire.beginTransmission(I2C_HIGHDRIVER_ADRESS);

void Highdriver_init(void) {

#define I2C_HIGHDRIVER_ADRESS (0x78) //Default adress for mp-Highdriver #define I2C_DEVICEID 0x00 #define I2C_POWERMODE 0x01 #define I2C_FREQUENCY 0x02 #define I2C_SHAPE 0x03 #define I2C_BOOST 0x04 #define I2C_PVOLTAGE 0x06 #define I2C_P1VOLTAGE 0x06 #define I2C_P2VOLTAGE 0x07 #define I2C_P3VOLTAGE 0x08 #define I2C_P4VOLTAGE 0x09 #define I2C_UPDATEVOLTAGE 0x0A #define I2C_AUDIO 0x05 extern boolean bPumpState[4]; extern uint8_t nPumpVoltageByte[4]; extern uint8_t nFrequencyByte;



```
Wire.write(I2C_POWERMODE); // Start Register 0x01
 Wire.write(0x01):
                               // Register 0x01 = 0x01 (enable)
 Wire.write(nFrequencyByte);
                              // Register 0x02 = 0x40 (100Hz)
 Wire.write(0x00); // Register 0x03 = 0x00 (sine wave)
 Wire.write(0x00);
                    // Register 0x04 = 0x00 (800 \text{KHz})
 Wire.write(0x00);
                    // Register 0x05 = 0x00 (audio off)
                    // Register 0x06 = Amplitude1
 Wire.write(0x00);
 Wire.write(0x00);
                    // Register 0x07 = Amplitude2
 Wire.write(0x00);
                    // Register 0x08 = Amplitude3
                    // Register 0x09 = Amplitude4
 Wire.write(0x00);
 Wire.write(0x01);
                     // Register 0x0A = 0x01 (update)
 Wire.endTransmission();
 bPumpState[0] = false;
 nPumpVoltageByte[0] = 0x1F;
float temp = _voltage; temp*=31.0f; temp/=250.0f; \frac{1}{250}Vpp = 0x1F
 nPumpVoltageByte[0] = constrain(temp,0,31);
 Wire.beginTransmission(I2C_HIGHDRIVER_ADRESS);
 Wire.write(I2C_PVOLTAGE);
 Wire.write(0);
 Wire.write(0);
 Wire.write(0);
 Wire.write((bPumpState[0] ? nPumpVoltageByte[0] : 0));
 Wire.write(0x01);
                        // update register values
 Wire.endTransmission();
}
void Highdriver_setvoltage(void) {
                                  // for pump toggle, if bPumpState[0] was changed previously
 Wire.beginTransmission(I2C_HIGHDRIVER_ADRESS);
 Wire.write(I2C_PVOLTAGE);
 Wire.write(0);
 Wire.write(0);
 Wire.write(0);
 Wire.write((bPumpState[0] ? nPumpVoltageByte[0] : 0));
 Wire.write(0x01);
                        // update register values
 Wire.endTransmission();
}
void Highdriver_setfrequency(uint16_t _frequency) {      // set pump frequency (_frequency [Hz])
if (_frequency>=800) {
  nFrequencyByte=0xFF;
 } else if (_frequency>=400) {
                                  // 400-800 Hz
  _frequency-=400;
  _frequency*=64;
  _frequency/=400;
  nFrequencyByte = \_frequency | 0xC0;
 } else if (_frequency>=200) {
                                  // 200-400 Hz
  _frequency-=200;
  _frequency*=64;
  frequency/=200;
  nFrequencyByte = \_frequency | 0x80;
                                  // 100-200 Hz
 } else if (_frequency>=100) {
  _frequency-=100;
  _frequency*=64;
  _frequency/=100;
  nFrequencyByte = \_frequency | 0x40;
 } else if (_frequency>=50) {
                                  // 50-100 Hz
  _frequency-=50;
  _frequency*=64;
  _frequency/=50;
  nFrequencyByte = \_frequency|0x00;
```



```
// outside of valid area
} else {
nFrequencyByte=0x00;
 Wire.beginTransmission(I2C_HIGHDRIVER_ADRESS);
 Wire.write(I2C_FREQUENCY);
 Wire.write(nFrequencyByte);
 Wire.endTransmission();
}
         Example code for the mp-Highdriver4
3.3.6
#define I2C_HIGHDRIVER_ADRESS (0x78) //Default adress for mp-Highdriver4 11110XX
#define I2C_DEVICEID
                         0x00
#define I2C_POWERMODE
                            0x01
#define I2C_FREQUENCY
                           0x02
#define I2C_SHAPE
                        0x03
#define I2C_BOOST
                        0x04
#define I2C_PVOLTAGE
                          0x06
#define I2C_P1VOLTAGE
                          0x06
#define I2C_P2VOLTAGE
                          0x07
#define I2C_P3VOLTAGE
                          0x08
#define I2C_P4VOLTAGE
                          0x09
#define I2C_UPDATEVOLTAGE 0x0A
#define I2C_AUDIO
extern boolean bPumpState[4];
extern uint8_t nPumpVoltageByte[4];
extern uint8_t nFrequencyByte;
void Highdriver4_init(void) {
                                    // Initialize mp-Highdriver
 Wire.beginTransmission(I2C_HIGHDRIVER_ADRESS);
 Wire.write(I2C_POWERMODE);
                                   // Start Register 0x01
 Wire.write(0x01);
                               // Register 0x01 = 0x01 (enable)
 Wire.write(nFrequencyByte); // Register 0x02 = 0x40 (100Hz)
                      // Register 0x03 = 0x00 (sine wave)
 Wire.write(0x00);
                      // Register 0x04 = 0x00 (800KHz)
 Wire.write(0x00);
                      // Register 0x05 = 0x00 (audio off)
 Wire.write(0x00);
 Wire.write(0);
                      // Register 0x06 = Amplitude1
Wire.write(0);
                      // Register 0x07 = Amplitude2
 Wire.write(0);
                      // Register 0x08 = Amplitude3
 Wire.write(0);
                      // Register 0x09 = Amplitude4
 Wire.write(0x01);
                      // Register 0x0A = 0x01 (update)
 Wire.endTransmission();
 bPumpState[0] = false;
 bPumpState[1] = false;
 bPumpState[2] = false;
 bPumpState[3] = false;
 nPumpVoltageByte[0] = 0x1F;
 nPumpVoltageByte[1] = 0x1F;
 nPumpVoltageByte[2] = 0x1F;
 nPumpVoltageByte[3] = 0x1F;
void Highdriver4_setvoltage(uint8_t _pump, uint8_t _voltage) { // Set new amplitude (_pump [#], _voltage [Vpp])
 float temp = _voltage; temp*=31.0f; temp/=250.0f; \frac{1}{250}Vpp = 0x1F
 if (_pump>=1 && _pump<=4) nPumpVoltageByte[_pump-1]=constrain(temp,0,31);
 Wire.beginTransmission(I2C_HIGHDRIVER_ADRESS);
 Wire.write(I2C PVOLTAGE);
 Wire.write((bPumpState[0] ? nPumpVoltageByte[0] : 0));
 Wire.write((bPumpState[1] ? nPumpVoltageByte[1] : 0));
 Wire.write((bPumpState[2] ? nPumpVoltageByte[2] : 0));
 Wire.write((bPumpState[3]? nPumpVoltageByte[3]: 0));
```



```
Wire.write(0x01);
                                                  // update register values
 Wire.endTransmission();
}
void Highdriver4_setvoltage(void) {
                                                         // for pump toggle, if bPumpState[] was changed previously
 Wire.beginTransmission(I2C_HIGHDRIVER_ADRESS);
 Wire.write(I2C_PVOLTAGE);
 Wire.write((bPumpState[0] ? nPumpVoltageByte[0] : 0));
 Wire.write((bPumpState[1] ? nPumpVoltageByte[1] : 0));
 Wire.write((bPumpState[2] ? nPumpVoltageByte[2] : 0));
 Wire.write((bPumpState[3]? nPumpVoltageByte[3]: 0));
                           // update register values
 Wire.write(0x01);
 Wire.endTransmission();
void Highdriver4_setfrequency(uint16_t _frequency) {
 if (_frequency>=800) {
  nFrequencyByte=0xFF;
 } else if (_frequency>=400) {
                                  // 400-800 Hz
  _frequency-=400;
  _frequency*=64;
  _frequency/=400;
  nFrequencyByte = \_frequency | 0xC0;
 } else if (_frequency>=200) {
                                  // 200-400 Hz
  _frequency-=200;
  _frequency*=64;
  _frequency/=200;
  nFrequencyByte = \_frequency|0x80;
 } else if (_frequency>=100) {
                                    // 100-200 Hz
  _frequency-=100;
  _frequency*=64;
  _frequency/=100;
  nFrequencyByte = \_frequency|0x40;
 } else if (_frequency>=50) {
                                    // 50-100 Hz
  _frequency-=50;
  _frequency*=64;
  _frequency/=50;
  nFrequencyByte = \_frequency|0x00;
                                    // outside of valid area
 } else {
  nFrequencyByte=0x00;
 Wire.beginTransmission(I2C_HIGHDRIVER_ADRESS);
 Wire.write(I2C_FREQUENCY);
 Wire.write(nFrequencyByte);
 Wire.endTransmission();
}
```

3.4 Lowdriver platform: mp-Lowdriver

The mp-Lowdriver is a small, easy to use driving circuit developed for the micropumps of the mp6-series especially for low-flowrate applications as it has a high amplitude resolution and is thus ideally suited for controlled loop flow regulation systems. It generates amplitudes up to 150 Vpp from a 3-5 V DC supply. The mp-Lowdriver comes with a high range of frequencies, but take into account that we only guarantee proper working conditions of the mp6 micropump inbetween 0 – 800 Hz.

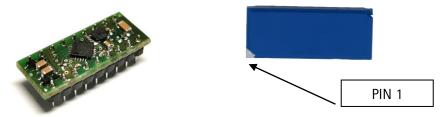
Its low power consumption makes it ideal for battery powered handheld devices or even solar powered devices.

The module can be integrated into a PCB design as a 18 pin DIL package.

The I²C interface allows the user to adapt frequency, amplitude and signal-shape to its application by the use of any simple microcontroller capable of I²C communication.



In order to locate Pin 1, please refer to the following figure. The pin is marked with a colored spot or triangular marking on the corner of the PCB.



3.4.1 Technical specifications mp-Lowdriver

Tabelle 1 mp6 micropump series has only been tried and tested up to 800 Hz. The use of frequencies above 800 Hz is not advised

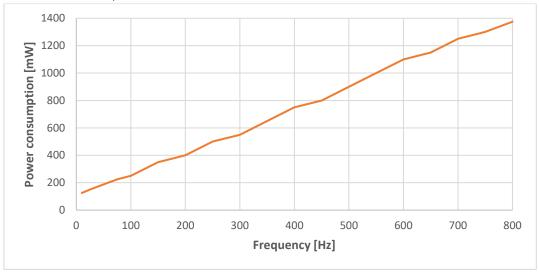
mp-Lowdriver controller	Order code: mp-Lowdriver
Dimensions	10.16 x 25.4 x 2.64 mm
	0.4 x 1.0 x 0.10 in
Adjustable parameters	amplitude, frequency, signal shape
Amplitude range	0 – 150 Vpp
	in 255 steps
Frequency range	8 – 2000 Hz ¹
	in 7.8125 Hz steps
Signal form	sine, custom
Power supply	3 – 5.5 V DC
	(6.0V absolute maximum rating)
Pin arrangement	DIL 18; horizontal 2.54 mm, vertical 7.62 mm

3.4.2 Electrical Characteristics

Parameter	Symbol	Conditions	Min	Тур.	Max	Unit
Supply voltage	VDD		3.0		5.5	V
Average current consumption	IDD	VDD = 5 V		40		mA
Voltage at pump	Vpump	VDD = 5 V	0		150	Vpp
Frequency range	Fpump	VDD = 5 V	7.8125		1992.1875	Hz
Digital Low-Signal					0.5	V
Digital High-Signal			1.4			V
Digital input current					1	μΑ
Shutdown current		VDD = 3.6V STANDBY=1		10		μΑ



3.4.3 Power consumption



3.4.4 Pin description

Pin	Name	Function				
1, 2, 4, 5,				IC1		
6, 7, 12,	NC	These pins should not be connected and left floating	_1_	DNC	IN-	18
15			_2	NC	IN+	<u>17</u>
3	GND	Ground	_	110	2.4.	
8	P1+	Piezo actuator 1, positive electrode (see schematic 1)	_3_	6ND	VDD	16
9	P1-	Piezo actuator 1, negative electrode (see schematic 1)		NC	NC	<u>15</u>
10	P2-	Piezo actuator 2, negative electrode (see schematic 1)	_5	NC	SDA	14
11	P2+	Piezo actuator 2, positive electrode (see schematic 1)		""	OLA I	l
10	CCI	Serial-Clock Input. SCL requires an external pullup	_6_	NC	SCL	13
13	SCL	resistor.	_7_	NC	NC	12
1.4	CDA	Open-Drain, Serial Data Input/Output. SDA requires an	_8_	.		11
14	SDA	external pullup resistor.		P1+	P2+	
16	VDD	Input Supply voltage	_9_	P1-	P2-	18
17	IN+	Analog input voltage positive (WIP1)		MP-LOWDRIVER		
18	IN-	Analog input voltage negative (WIP1)				

¹WIP: work in progress. Analog inputs are not implemented yet. There is no documentation currently available for the use of this feature.



3.4.5 Register Map

Table 19 mp-Lowdriver register map

	Lowariver regis										
REG NO.	DEFAULT	BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0		
0x00	0x02		Reserved ILLEGAL_ADDR FIFO_EMPTY FIFO_F								
0x01	0x38		Re	eserved			INPUT_MUX	GAIN[1	:0]		
0x02	0x40	DEV_RST	STANDBY	Rese	rved	TIM	MEOUT[1:0]	EN_OVERRIDE	GO		
0x03	0x00				W	AVEFORN	/IO[7:0]				
0x04	0x00				W	AVEFORN	И1[7:0]				
0x05	0x00		WAVEFORM2[7:0]								
0x06	0x00				W	AVEFORN	/I3[7:0]				
0x07	0x00				W	'AVEFORN	Л4[7:0]				
0x08	0x00				W	'AVEFORN	Л5[7:0]				
0x09	0x00		WAVEFORM6[7:0]								
0x0A	0x00		WAVEFORM7[7:0]								
0x0B	0x00		FIFO[7:0]								
0xFF	0x00					PAGE[7	·:0]				

Table 20 mp-Lowdriver register 0x00

Bit	Field	Type	Default	Value	Description
7-3	Reserved				
2	ILLEGAL_ADDR	RO	0		Indicates that the waveform generator attempted to perform an illegal operation. This usually means that the user entered improper header information in the waveform memory.
				0	Normal operation
				1	Illegal address attempted
1	FIFO_EMPTY	RO	1		Indicates that the internal 100-byte FIFO is empty.
				0	FIFO is not empty
				1	FIFO is empty
0	FIFO_FULL	RO	0		Indicates that the internal 100-byte FIFO is full and cannot accept data until another byte has played through the internal DAC.
				0	FIFO not full
				1	FIFO is full

Table 21 mp-Lowdriver register 0x01

Bit	Field	Туре	Default	Value	Description
7-3	Reserved				
2	INPUT_MUX[0]	RW	0		Selects the source to be played.
				0	Digital input source
				1	Analog input source
1-0	GAIN[1:0]	RW	0		Selects the gain for the amplifier.
				0	25 V (Digital) - 28.8 dB (Analog)
				1	50 V (Digital) - 34.8 dB (Analog)
				2	75 V (Digital) - 38.4 dB (Analog)
				3	100 V (Digital) - 40.7 dB (Analog)



in process, reset all of its internal register to their default of and enters standby mode. O Normal Operation	Bit Field		Type	Default	Value	Description		
and enters standby mode. O Normal Operation	7 DEV_	RST	RW	0		When asserted, the device will immediately stop any transaction		
STANDBY RW 1 Low-power standby 0 Device is active and ready to receive a signal 1 Device is in low-power standby mode 1 Device is active and ready to receive a signal Device is in low-power standby mode 1 Device is in low-power standby mode 1 Device is active and ready to receive a signal Device is active and ready to receive						in process, reset all of its internal register to their default values,		
Reset device								
6 STANDBY RW 1 Low-power standby Device is active and ready to receive a signal 1 Device is in low-power standby mode 5- 4 Reserved 3- Time period when the FIFO runs empty and the device goe idle mode, powering down the boost converter and amplifier 0 5 ms 1 10 ms 2 15 ms 3 20 ms 1 EN_OVERRIDE RW 0 Override bit for the boost converter and amplifier enables. Boost converter and amplifier enables are controlled by logic 1 Boost converter and amplifier are enabled indefinitely O GO RW 0 Starts waveform playback, as indicated by the sequence recoval and the device goe idle mode, powering down the boost converter and amplifier enables. Starts waveform playback, as indicated by the sequence recoval and the device goe idle mode, powering down the boost converter and amplifier enables. Starts waveform playback, as indicated by the sequence recoval and the device goe idle mode, powering down the boost converter and amplifier enables. Starts waveform playback, as indicated by the sequence recoval and the device goe idle mode, powering down the boost converter and amplifier enables. Starts waveform playback, as indicated by the sequence recoval and the device goe idle mode, powering down the boost converter and amplifier enables. Starts waveform playback, as indicated by the sequence recoval and the device goe idle mode, powering down the boost converter and amplifier enables. Starts waveform playback, as indicated by the sequence recoval and the device goe idle mode, powering down the boost converter and amplifier enables.					0	Normal Operation		
O Device is active and ready to receive a signal 1 Device is in low-power standby mode 5-4 Reserved 3- TIMEOUT[1:0] RW O Time period when the FIFO runs empty and the device goe idle mode, powering down the boost converter and amplifier on 5 ms 1 10 ms 2 15 ms 3 20 ms 1 EN_OVERRIDE RW O Override bit for the boost converter and amplifier enables. Boost converter and amplifier enables are controlled by logic 1 Boost converter and amplifier are enabled indefinitely O GO RW O Starts waveform playback, as indicated by the sequence recovered by the sequence rec					1	Reset device		
Time period when the FIFO runs empty and the device goe idle mode, powering down the boost converter and amplified to 5 ms Time period when the FIFO runs empty and the device goe idle mode, powering down the boost converter and amplified to 5 ms 1 10 ms 2 15 ms 3 20 ms 1 EN_OVERRIDE RW O Override bit for the boost converter and amplifier enables. Boost converter and amplifier enables are controlled by logic 1 Boost converter and amplifier are enabled indefinitely O GO RW O Starts waveform playback, as indicated by the sequence recovered and self-clears upon completion of plath the user may optionally clear this bit to cancel waveform playback, and self-clears upon completion of plath the user may optionally clear this bit to cancel waveform playback, and self-clears upon completion of plath the user may optionally clear this bit to cancel waveform playback.	6 STAN	DBY	RW	1		Low-power standby		
5-4 Reserved 3-TIMEOUT[1:0] RW 0 Time period when the FIFO runs empty and the device goe idle mode, powering down the boost converter and amplified 0 5 ms 1 10 ms 2 15 ms 3 20 ms 1 EN_OVERRIDE RW 0 Override bit for the boost converter and amplifier enables. Boost converter and amplifier enables are controlled by logic 1 Boost converter and amplifier are enabled indefinitely O GO RW 0 Starts waveform playback, as indicated by the sequence recovol ox03 through 0x0A. This bit remains high during the executive waveform playback, and self-clears upon completion of pla The user may optionally clear this bit to cancel waveform playback.					0	Device is active and ready to receive a signal		
Reserved					1	Device is in low-power standby mode		
TIMEOUT[1:0] RW 0 Time period when the FIFO runs empty and the device goe idle mode, powering down the boost converter and amplified 0 5 ms 1 10 ms 2 15 ms 3 20 ms 1 EN_OVERRIDE RW 0 Override bit for the boost converter and amplifier enables. Boost converter and amplifier enables are controlled by logic 1 Boost converter and amplifier are enabled indefinitely O GO RW 0 Starts waveform playback, as indicated by the sequence reduced waveform playback, and self-clears upon completion of plate The user may optionally clear this bit to cancel waveform playback.	5-							
idle mode, powering down the boost converter and amplified 0 5 ms 1 10 ms 2 15 ms 3 20 ms 1 EN_OVERRIDE RW 0 Override bit for the boost converter and amplifier enables. Boost converter and amplifier enables are controlled by logic 1 Boost converter and amplifier are enabled indefinitely O GO RW 0 Starts waveform playback, as indicated by the sequence red 0x03 through 0x0A. This bit remains high during the execut waveform playback, and self-clears upon completion of pla The user may optionally clear this bit to cancel waveform playback.								
0 5 ms 1 10 ms 2 15 ms 3 20 ms 1 EN_OVERRIDE RW 0 Override bit for the boost converter and amplifier enables. Boost converter and amplifier enables are controlled by logic 1 Boost converter and amplifier are enabled indefinitely O GO RW 0 Starts waveform playback, as indicated by the sequence required ox		OUT[1:0]	RW	0		Time period when the FIFO runs empty and the device goes into		
1 10 ms 2 15 ms 3 20 ms 1 EN_OVERRIDE RW 0 Override bit for the boost converter and amplifier enables. Boost converter and amplifier enables are controlled by logic 1 Boost converter and amplifier are enabled indefinitely O GO RW 0 Starts waveform playback, as indicated by the sequence recovered by t	2	2						• • •
2 15 ms 3 20 ms 1 EN_OVERRIDE RW 0 Override bit for the boost converter and amplifier enables. Boost converter and amplifier enables are controlled by logic 1 Boost converter and amplifier are enabled indefinitely O GO RW 0 Starts waveform playback, as indicated by the sequence reconvolution of playback, and self-clears upon completion of playback.						0	5 ms	
3 20 ms 1 EN_OVERRIDE RW 0 Override bit for the boost converter and amplifier enables. Boost converter and amplifier enables are controlled by logic 1 Boost converter and amplifier are enabled indefinitely O GO RW 0 Starts waveform playback, as indicated by the sequence recovered by the sequence rec						1	10 ms	
1 EN_OVERRIDE RW 0 Override bit for the boost converter and amplifier enables. Boost converter and amplifier enables are controlled by logic Boost converter and amplifier are enabled indefinitely O GO RW O Starts waveform playback, as indicated by the sequence reconverter and supplied and the sequence reconverter and su					2	15 ms		
Boost converter and amplifier enables are controlled by logic 1 Boost converter and amplifier are enabled indefinitely O GO RW O Starts waveform playback, as indicated by the sequence reconverter and self-clears upon completion of playback, and self-clears upon completion of playback, and self-clears this bit to cancel waveform playback, and self-clears upon completion of playback.					3	20 ms		
0 logic 1 Boost converter and amplifier are enabled indefinitely O GO RW O Starts waveform playback, as indicated by the sequence reconverted	1 EN_C	VERRIDE	RW	0		Override bit for the boost converter and amplifier enables.		
Boost converter and amplifier are enabled indefinitely O GO RW O Starts waveform playback, as indicated by the sequence reconstruction of playback, and self-clears upon completion of playback.						Boost converter and amplifier enables are controlled by device		
O GO RW O Starts waveform playback, as indicated by the sequence red 0x03 through 0x0A. This bit remains high during the execut waveform playback, and self-clears upon completion of pla The user may optionally clear this bit to cancel waveform.					0	logic		
0x03 through 0x0A. This bit remains high during the execut waveform playback, and self-clears upon completion of pla The user may optionally clear this bit to cancel wave					1	Boost converter and amplifier are enabled indefinitely		
waveform playback, and self-clears upon completion of pla The user may optionally clear this bit to cancel wav	0 GO		RW	0		Starts waveform playback, as indicated by the sequence registers		
The user may optionally clear this bit to cancel way						0x03 through 0x0A. This bit remains high during the execution of		
						waveform playback, and self-clears upon completion of playback.		
playback.						The user may optionally clear this bit to cancel waveform		
0 No waveform playing					0	No waveform playing		
1 Play waveform					1	Play waveform		

Table 23 mp-Lowdriver register 0x03 - 0x0A

Table 2	3 mp-Lowariver register	UXU3 -	UXUA	
Bit	Field	Type	Default	Description
7-0	WAVEFORM[7:0]	RW	0	When the GO bit is asserted, the waveform processing engine will go to register address 0x03 and play the waveform ID that is indicated there. After completion of that waveform, the engine proceeds to register address 0x04 to play that waveform ID. If the ID value is zero, the playback process terminates. Otherwise, this process repeats until it finds a waveform ID of zero, or all 8 waveforms are played.

Table 24 mp-Lowdriver register 0x0B

Bit	Field	Type	Default	Description
7-	FIF0[7:0]	RW	0	Entry point for FIFO data. The user repeatedly writes this register with
0				continuous waveform data.

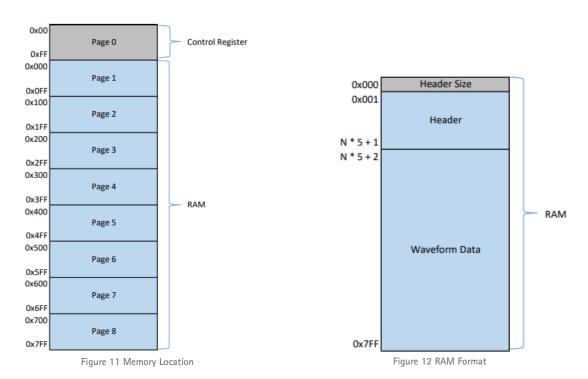
Table 25 mp-Lowdriver register 0xFF

Bit	Field	Type	Default	Description
7-	PAGE[7:0]	RW	0	Page register for memory interface. Write this register with the memory
0				page to be accessed.



3.4.6 Memory Map

Because the device addresses are only 8-bits, a special exception exists to distinguish whether the user is trying to write the page register on the control page at address 0xFF or the memory page at location 0x1FF. In order to access the page register, the programmer must use a Single-Byte I2C protocol to perform a single-byte write to memory location 0xFF.



Header Size: First byte at memory location 0x000 must contain the header size and refers to the last header byte (N * 5 + 1) with N being

the number of stored waveforms
Header: Contains blocks of 5 bytes

The start address, upper byte Start address, lower byte Stop address, lower byte Stop address, lower byte Stop address, lower byte Repeat count Start address of 5 bytes Bit:

7 6 5 4 3 2 1 0

Reserved Page Number

Address within Page

Repeat count Address within Page

Number of repeats for addressed Waveform

Mode: A 0 indicates direct Playback from RAM, and a 1 indicates the data for Waveform Synthesis for a sinusoid signal

Figure 13 Header Format

3.4.7 Waveform Synthesis

The mp-Lowdriver possess a waveform synthesizer for sinusoid signal. This allows the microcontroller to drive a pump with minimum amount of workload. A package of four data bytes representing amplitude, frequency, cycles and envelope need to be stored inside the mp-Lowdriver.

Amplitude

The amplitude byte refers to the magnitude of the synthesized sinusoid. 0xFF produces a full-scale sinusoid, 0x80 produces a half-scale sinusoid, and 0x00 does not produce any signal. To calculate the absolute peak voltage, use the following equation, where amplitude is a single-byte integer:

Peak voltage = amplitude / 255 x full-scale peak voltage

Frequency

The frequency byte adjusts the frequency of the synthesized sinusoid. The minimum frequency is 7.8125 Hz. A value of zero is not allowed. The sinusoidal frequency is determined with the following equation, where frequency is a single-byte integer:

Sinusoid frequency (Hz) = 7.8125 x frequency



Cycles

The number of sinusoidal cycles to be played by the synthesizer. A convenient way to specify the duration of a coherent sinusoid is by inputting the number of cycles. This method ensures that the waveform chunk will always begin and end at zero amplitude, thus avoiding discontinuities. The actual duration in time given by this value may be calculated through the following equation, where # of cycles and frequency are both single-byte integers. $Duration (ms) = 1000 \times \# of cycles / (7.8125 \times frequency)$

Envelope

The envelope byte is divided into two nibbles. The upper nibble, bits [7:4], sets the ramp-up rate at the beginning of the synthesized sinusoid, and the lower nibble, bits [3:0], sets the ramp-down rate at the end of the synthesized sinusoid. The user must note that the ramp-up time is included in the duration parameter of the waveform, and the ramp-down time is appended to the duration parameter of the waveform. As such, if a ramp-up time is used, the ramp-up time must be less than the duration time as programmed in byte 3. ramp to full-scale amplitude (amplitude = 0xFF). Ramps to a fraction of full-scale have the same Also note that the Total Ramp Time is for a fraction of the Total Ramp Time.

Table 26 Envelope ramp-up times

Table 26 Envelope ramp-up times					
Nibble Value	Total Ramp Time				
0	No Envelope				
1	32 ms				
2	64 ms				
3	96 ms				
4	128 ms				
5	160 ms				
6	192 ms				
7	224 ms				
8	256 ms				
9	512 ms				
10	768 ms				
11	1024 ms				
12	1280 ms				
13	1536 ms				
14	1792 ms				
15	2048 ms				

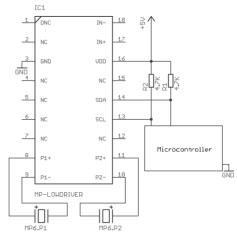
The Example code shows how to initialize the mp-Lowdriver as well as a way to change the amplitude and frequency.

3.4.8 Operation with variable settings via I²C-Interface

The mp-Lowdriver can only be operated using the I²C-Interface. Using this interface all the features of the chip can be accessed. A complete interface and protocol description will be released on our website in the future. At the moment the mp-Multiboard source-code is the best source for initializing and changing parameters of the mp-Lowdriver using the I²C-Interface. The source-code is available for download on our website. An excerpt is included in the last chapter of this document.

Use external pullup resistors for the SDA and SCL signals to set the logic-high level for the bus. Pullup resistors with values between 660Ω and $4.7~k\Omega$ are recommended. Do not allow the SDA and SCL voltages to exceed the mp-Lowdriver supply voltage VDD. The mp-Lowdriver device operates as an I2C-slave with 1.8-V logic thresholds, but can operate up to the VDD voltage.





Schematic 6 Controlling the mp-Lowdriver via I²C

3.4.9 Slave address

The slave address for the device is 0x59 (7-bit), or 1011001 in binary, which is equivalent to 0xB2 (8-bit) for writing and 0xB3 (8-bit) for reading.



```
3.4.10 Example code for mp-Lowdriver
#define I2C_LOWDRIVER_ADRESS (0x59) //Defaultadress for Lowdriver 1011001
extern boolean bPumpState[4];
extern uint8_t nPumpVoltageByte[4];
extern uint8_t nFrequencyByte;
void selectControlRegisters() {
Wire.beginTransmission(I2C_LOWDRIVER_ADRESS);
Wire.write(0xFF);
Wire.write(0x00);
Wire.endTransmission();
}
void selectMemoryRegisters() {
Wire.beginTransmission(I2C_LOWDRIVER_ADRESS);
Wire.write(0xFF);
Wire.write(0x01);
Wire.endTransmission();
}
void Lowdriver_init() {
selectControlRegisters();
Wire.beginTransmission(I2C_LOWDRIVER_ADRESS);
Wire.write(0x01); //Select (Control) Register 0x01
Wire.write(0x02); //Set Gain 0-3 (0x00-0x03 25v-100v)
Wire.write(0x00); //Take device out of standby mode
Wire.write(0x01); //Set sequencer to play WaveForm ID #1
Wire.write(0x00); //End of sequence
Wire.endTransmission();
selectMemoryRegisters();
Wire.beginTransmission(I2C_LOWDRIVER_ADRESS);
Wire.write(0x00); //Select Register 0x00
Wire.write(0x05); //Header size -1
Wire.write(0x80); //Start address upper byte (page), also indicates Waveform Synthesis Mode
Wire.write(0x06); //Start address lower byte (in page address)
Wire.write(0x00); //Stop address upper byte
Wire.write(0x09); //Stop address Lower byte
Wire.write(0x00); //Repeat count, 0 = infinite loop
Wire.write((bPumpState[2] ? nPumpVoltageByte[2] : 0)); //Amplitude
Wire.write(0x0C); //Frequency. (100Hz)
Wire.write(100); //cycles
Wire.write(0x00); //envelope
Wire.endTransmission();
delay(10);
```



```
Data sheet: Electronics for the mp6
Version 1.2 (November 2022)
selectControlRegisters();
Wire.beginTransmission(I2C LOWDRIVER ADRESS);
Wire.write(0x02); //Set page register to control space
Wire.write(0x01); //Set GO bit (execute WaveForm sequence)
Wire.endTransmission();
}
void Lowdriver_setvoltage(uint8_t _voltage) {
nPumpVoltageByte[2]=_voltage;
Wire.beginTransmission(I2C_LOWDRIVER_ADRESS);
Wire.write(0x02); //Stop Waveform playback
Wire.write(0x00);
Wire.endTransmission();
selectMemoryRegisters();
Wire.beginTransmission(I2C_LOWDRIVER_ADRESS);
Wire.write(0x06); //Set page register to control space
Wire.write((bPumpState[2] ? nPumpVoltageByte[2] : 0)); //0-255
Wire.endTransmission();
delay(10);
selectControlRegisters();
Wire.beginTransmission(I2C_LOWDRIVER_ADRESS);
Wire.write(0x02); //Start Waveform playback
Wire.write(0x01);
Wire.endTransmission();
}
void Lowdriver_setfrequency(uint16_t _frequency) {
float temp = _frequency; temp/=7.8125;
nFrequencyByte = temp;
if (nFrequencyByte==0) nFrequencyByte=1;
Wire.beginTransmission(I2C LOWDRIVER ADRESS);
Wire.write(0x02); //Stop Waveform playback
Wire.write(0x00);
Wire.endTransmission();
selectMemoryRegisters();
Wire.beginTransmission(I2C LOWDRIVER ADRESS);
Wire.write(0x07); //Set page register to control space
Wire.write(nFrequencyByte); //0-255
Wire.endTransmission();
delay(10);
selectControlRegisters();
Wire.beginTransmission(I2C LOWDRIVER ADRESS);
Wire.write(0x02); //Start Waveform playback
Wire.write(0x01);
Wire.endTransmission();
```

}



3.5 mp-Driver

The mp-Driver is a small, easy to use driving circuit developed for the micropumps of the mp6-series. It generates up to 270 Vpp voltage from a 3-5 V supply. Its low power consumption makes it ideal for battery powered handheld devices or even solar powered devices. The module can be integrated into a PCB design like a 14 pin DIL package In order to locate Pin 1, please refer to the following figure. The pin is marked with a white spot



3.5.1 Technical specifications mp-Driver

mp-Driver					
The mp-Driver drives the micropump at adjustable performance in a package similar to an integrated circuit. It					
enables integration into system electronics or on a PCB.					
Dimensions	10,5 x 20,5 x 6 mm				
	0.4134 x 0.8070 x 0.2362 in.				
Adjustable parameters	amplitude, frequency				
Amplitude range	85 – 250 Vpp ¹				
Frequency range	25 - 226 Hz				
	(Frequencies up to 1000 Hz are possible; please note that the amplitude				
	will decrease in that case. Frequencies down to 1 Hz are possible with an				
	external clock generator.)				
Signal form	similar to rectangular				
Power supply	2.5 – 5.5 VDC				
	(5 V recommended for optimized performance)				
Current consumption	ca. 30 mA at 5 V				
Pin arrangement	DIL 14; horizontal ~2.54 mm, vertical ~7.62 mm				
Operating temperature	Between 0°-80°C				
¹amplitude range can go up to 270 Vpp but guaranteed is 250 Vpp					

3.5.2 Electrical Characteristics

Parameter	Symbol	Conditions	Min	Тур.	Max	Unit
Supply voltage	VDD		2.5	5	5.5	V
Average current consumption	IDD			30		mA
Setting range AMPLITUDE			0.35		1.3	VDC
min. voltage at pump	Vpump	AMPLITUDE =	80	85	90	Vpp
		0.5 VDC				
max. voltage at pump (2)	Vpump	AMPLITUDE =	240	250	270	Vpp
		1.3 VDC				
PWM-range AMPLITUDE			0.2		1	MHz
Frequency output	F		90	100	110	Hz
Digital Low-Signa				0		V
Digital High-Signal			2		VDD	V
Capacitance CLOCK			1.0	2.2	10	nF
Input current AMPLITUDE			1		3	μΑ
Operating current during				1.6		μΑ
Shutdown Mode						

²The signal output is determined by internal components



3.5.3 Pin Layout

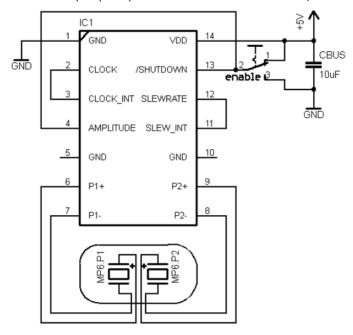
Pin	Name	Function				
1, 5, 10	GND	Ground				
2	CLOCK	Predefined clock signal. Frequency will be set to the nominal 100 Hz, when this pin is connected to CLOCK_INT. (The internal capacitor is 2.2 nF.) A capacitor of 1 to 10 nF can be placed between CLOCK and GND to define other frequencies (see Figure 15)It is also possible to connect an external clock signal with a quadruplicated frequency of the micropumps frequency.	_1_	GND	VDD	14
3	CLOCK_INT	When connected to CLOCK the frequency is set to 100 Hz.	2	CLOCK	/SHUTDOWN	13
4	AMPLITUDE	The amplitude can be set either with an analogue voltage between 0.35 V to 1.3 V, or with an equivalent pulsewidth modulation (PWM) at a voltage level of 5 V and a pulse frequency of 0.2 to 1 MHz.	4		SLEWRATE SLEW_INT GND	11 10
6	P1+	Piezo actuator 1, positive electrode		0110	OND	
7	P1-	Piezo actuator 1, negative electrode	6	P1+	P2+	9
8	P2-	Piezo actuator 2, negative electrode	7	P1-	P2-	8
9	P2+	Piezo actuator 2, positive electrode		F1-	F2·	
11	SLEW_INT	Internal resistor against GND for set the slew rate. (The internal resistor is 360 k Ω .)				
12	SLEWRATE	Setting of the slew rate. This pin will be connected to SLEW_INT				
13	/SHUTDOW N	If the micropump should be switched off, the mp6-OEM has to be set to the Shutdown Mode. Then AMPLITUDE and SHUTDOWN have to be set to 0 V.				
14	VDD	Supply voltage				



3.5.4 Examples of circuiting the mp-Driver

Operation with fixed settings

The mp-Driver can operate the micropumps of the mp6-series without further external components. In this case, frequency and amplitude to the micropump are predefined to 270 V and 100 Hz by the internal circuit.



Schematic 7 Predefined amplitude of 270 V and frequency of 100 Hz by internal circuit

Operation with variable settings via external components

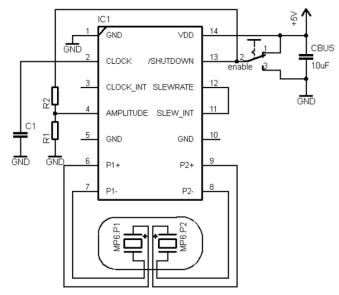
In this example, frequency and amplitude to the micropump will be defined by external components. The amplitude can be varied in the range from 85 Vpp to 270 Vpp, the frequency from 25 Hz to 226 Hz. With frequencies above 226 Hz it is possible that the maximal amplitude will decrease. Frequencies below 25 Hz are possible too, however, the stability of the signal form has to be verified then!

Using a potential divider it is possible to change the amplitude, see Figure 14 below. The voltage at the AMPLITUDE-Pin can be calculated with this formula:

$$V_{\scriptscriptstyle AMPLJTUDE} = V_{\scriptscriptstyle DD} \cdot \frac{R1}{R1 + R2}$$

As an alternative to the potential divider, an appropriate potentiometer (e.g. 10 k Ω) is possible too.





Schematic 8 Defining frequency and amplitude with external components

The relation of the voltage at the AMPLITUDE-Pin to the output voltage is shown in image Figure 14 below.

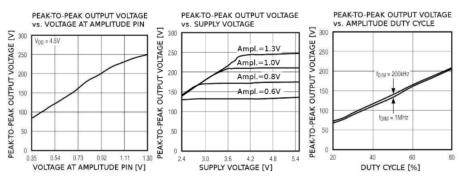


Figure 14 Output voltage changed with external components

Variation of frequency is realized with a capacitor of 1 to 10 nF. Typical capacitances are shown in the image Fehler! V erweisquelle konnte nicht gefunden werden. below.

For frequencies lower than 25 Hz an external frequency signal has to be uses, as explained in "Operation with variable settings via microcontroller"

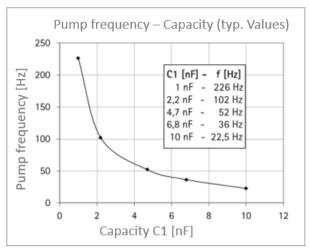


Figure 15 Typical capacitances for different



Operation with variable settings via microcontroller

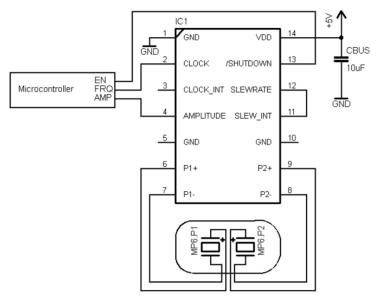
Using a microcontroller to operate the micropump, a quadruplicated frequency of the final micropumps frequency has to be connected to the CLOCK Pin. With this procedure, single-digit frequencies can be achieved.

Please take into account to set the pulse-width of the frequency duty cycle at a level of 95% when operating at frequencies lower than 25 Hz.

The amplitude can be set either with an analogue voltage between 0.35 V to 1.3 V, or with an equivalent pulse-width modulation (PWM) at a voltage level of 5 V and a pulse frequency of 0.2 to 1 MHz at the pin AMPLITUDE.

The PWM signal must have an amplitude of at least 1.3 V and maximal the same as the supply voltage to ensure that the output voltage is only dependent on the duty cycle!

For minimizing the power consumption, the mp-Driver can be switched off. One way is to set the mp-Driver into the Shutdown Mode; then AMPLITUDE and SHUTDOWN have to be set to 0 V. The other way is to interject the power supply.



Schematic 9 Microcontroller as an external component

Noise reduction

If the noise generated by the pump is critical, a series resistor of 2-10 $k\Omega$ in the P1+ and the P2+ line between the mp-Driver and the pump will help. There is no limit for the resistor value but it will decrease the maximum pump performance.

All values are approximate and no guarantee of specific technical properties.



3.6 Mp-Multiboard2

The mp-Multiboard is available in different combinations and has the following standard components included:

- mp-Multiboard2 board
- preprogrammed Esp32
- board housing
- micropump mp6
- Micro-USB cable



Figure 16 mp-Multiboard2

3.6.1 Setup instructions

Make sure pump driver, pump cable harness and pumps are plugged in correctly; install the esp32 USB driver; connect the Multiboard via USB cable to the computer; start the Multiboard2 App

Do not disconnect pumps or driver while Multiboard is powered (via USB or otherwise)!

For more information on the Multiboard App and how to use the Multiboard please have a look at mp6 Software Datasheet. (available in our download section https://www.bartels-mikrotechnik.de/downloads)

3.6.2 Electrical Characteristics

The Multiboard2 is powered via USB connection of the microcontroller. It does comply with the USB standard and does not require more than 500mA to operate under normal conditions. If in any case more current is needed on the board, an external power supply can be connected to the USB-C power supply connector. There is a separate power supply connector labeled "POWER" that has no function for now.



The USB-C power supply connector is directly wired to the RAW and GND pins of the microcontroller and tied to its onboard voltage regulator. Recommended input voltage for this connector is 5V. For the Connector labeled "POWER" the mate plug should have an outer diameter of 3,5 mm and an inner diameter to be able to accept the 1

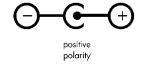


Figure 17 Connector Polarity



mm pin. See Figure 17 for connector polarity. Recommended input voltage for this connector is 12V. Note: this Connector is planned for future components that need supply voltages greater than 5V DC.

For all other characteristics of the microcontoller or the pump driver please refere to the corresponding manual or datasheet.

Information about the pump drivers and the mp6 micropumps is available on our download-page: https://www.bartels-mikrotechnik.de/downloads/

3.6.3 Pump drivers

The Multiboard2 is compatible to our mp-Lowdriver, mp-Driver, mp-Highdriver and mp-Highdriver4. Depending on the driver attached to the Multiboard, capabilities and ranges of amplitude and frequency can change. Please refer to the according manual for details. When attaching the pump driver make sure to follow the correct orientation as shown in the pictures below (notice the white dot indicating Pin 1).









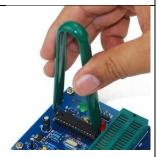
Mp-Highdriver4

Mp-Highdriver4 & Mp-Highdriver

Mp-Lowdriver

Mp-Driver

Use caution when removing the pump driver from the board in order to not bend the pins of the chip. IC pliers are recommended to be used for extraction.



3.6.4 Valve driver

The socket labeled "Valve driver" accepts our current dual valve driver that is able to drive two Takasago valves. The valves are current driven and the driver takes care of generating and supplying a constant current on two separate outputs on demand. The two outputs are digitally controlled via the Multiboard.

Attention: Since the valve driver needs a minimum of 4.5V to operate correctly, an external power supply needs to be attached to the Multiboard. The 5V rail of the Multiboard supplied by the microcontrollers USB is not reliable enough, to guarantee operation. See chapter 0 mp-valvedriver (T&M) for more details.

3.6.5 Sensors

The mp-Multiboard2 supports readout of digital flow and pressure sensors as well as liquid level sensors for fluid inline detection. The liquid flow-sensors of type SLF3S-0600F and SLF3S-1300F from the manufacturer Sensirion can be attached to the picoblade connector labeled "I2C 3.3V" via a straight 6-pin ribbon cable. Both sensors can be bought form our store with the ribbon cable included. For measuring pressure, we have implemented the readout for Honeywell ABP-series 15PSI digital pressure sensor. This one is sold on our store with a cable and 4-pin Grove connector already attached, fitting any of the the pinheader labeled "I2C 5V". The Multiboard can have up to three



analog sensors, e.g. the OPB350L125Z liquid level sensors from the manufacturer Optek. This one is sold on our store with a cable and 4-pin Grove connector already attached, fitting any of the the pinheader labeled "Analog".

3.6.6 Auxiliary connectors

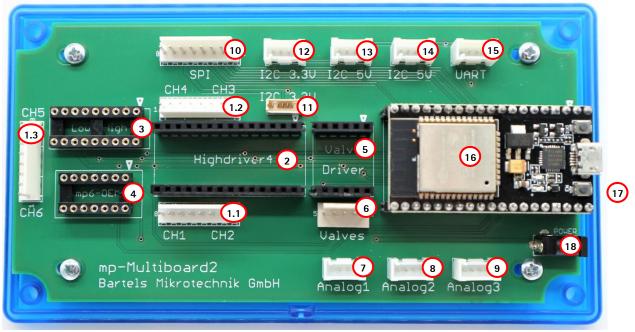


Figure 18 mp-Multiboard2 connectors

The Multiboard has multiple auxiliary connectors available. Some of these are not in use at the moment and will be documented in this manual once fully implemented. Nevertheless, here is a list of the connectors and their (future) use:

1.1,	CH1 / CH2,	Three 8-pin connectors for pump cable harness.	Mp6 Miicropumps
1.2,	CH3 / CH4,	Each cable harness has two FFC connectors on the	
1.3	CH5 / CH6	other end to connect to the micropump flex cable	
2	Mp-Highdriver4	Mp-Highdriver4 Socket	Mp-Highdriver4
3	Mp-Highdriver/	Mp-Highdriver & mp-Lowdriver Socket	Mp-Highdriver or mp-Lowdriver
	mp-Lowdriver		
4	Mp-Driver	Mp-Driver Socket	Mp-Driver
5	Mp-Valvedriver	Mp-Valvedriver Socket	Mp-ValvedriverT or Mp-ValvedriverM
6	Valves	5-pin connector for active valves	1x BV1101 or 2x SMV-2R-BN1F
7, 8, 9	Analog	4-pin Grove connector for an analog sensors with	
		5V supply voltage	
10	SPI	8-pin connectors for an SPI sensor	
11	I2C 3.3V (6-pin)	6-pin connector for an I ² C flow-sensor	SLF3S-1300F, SLF3S-0600F
12	I2C 3.3V (Grove)	4-pin Grove connecter for I ² C sensors with 3.3V	
		supply voltage	
13, 14	12C 5V	4-pin Grove connecter for I2C sensors with 5V	Honeywell ABP, SFS01
		supply voltage	
15	UART	4-pin Grove connecter for UART devices with 5V	
		supply voltage	
16	Micro Controller	ESP32 with Multiboard2 Firmware	ESP32
17	USB-C	External 5V power supply connector	External Power Supply
18	Power	Barrel power supply connector	



3.6.7 Package dimensions



Length 142,5mm [5.61 in]

Figure 19 Package Dimensions

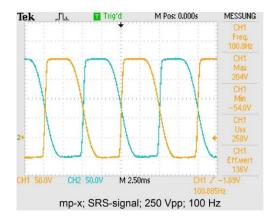
3.7 mp-Labtronix controller

mp-Labtronix controller	Order code: mp-Labtronix				
Access to the full range of evaluation of the micropum	of driving parameters. A system for the professional ps.				
Dimensions	7.5 x 16 x 20 cm 2.983 x 6.299 x 7.874 in.				
Weight	ca. 800 g				
Adjustable parameters	amplitude, frequency, signal form				
Amplitude range	0 – 250 V				
Frequency range	0 - 300 Hz				
Signal form	SRS, rectangular, sine				
Power supply	mains adaptor				
Current consumption	750 mA at 7.5 V				
USB-Port	one				
connectable micropumps	mp6-liq, mp6-gas, mp6-gas+, mp6-pi, mp6-pp: 1x				

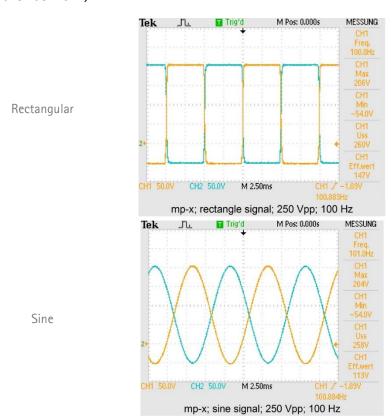


3.7.1 Electrical signal form

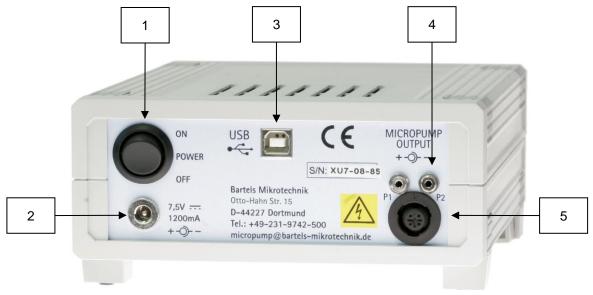
SRS







3.7.2 Connecting the pump to the mp-Labtronix



- 1 Main power switch
- 2 Power supply connector
- 3 USB interface for computer connection
- 4 Connections port for one or two mp5
- 5 Connection port for one micropumps of the mp6-series

Please note that it is only possible to connect either one micropump of the mp6-series or a maximum of two mp5 to the mp-Labtronix, otherwise a maximum voltage drop is possible!

<u>Step 1:</u> Plug the micropump control cable into the corresponding micropump connector.

<u>Step 2:</u> Check the mains adaptor plug polarity. It is pictured next to the power supply connector at the back of the controller. If the plug polarity is wrong, the controller cannot work.

Please make sure that the setting on the included connector is attuned to 7.5 V.



Step 3: Connect the mains adaptor with the power supply connection.

Step 4: Plug the mains adaptor into a mains socket.

Step 5: Now you can start the control unit with the main power switch.



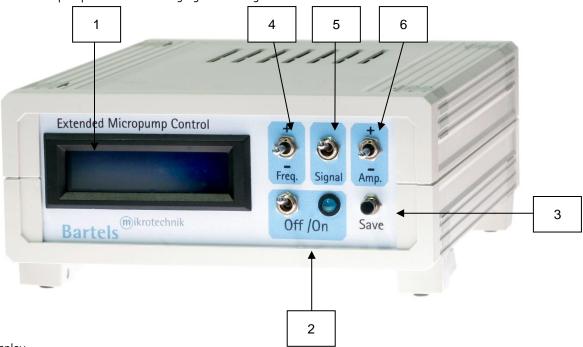
THE "MICROPUMP OUT" CONNECTOR CAN CARRY HIGH VOLTAGE!
PLUG IN THE MICROPUMP CONTROL CABLE ONLY WHEN THE MP-LABTRONIX CONTROLLER IS
SWITCHED OFF!

3.7.3 Operation of the mp-Labtronix

The mp-Labtronix provides three parameters to be selected independently of each other to control the micropumps:

- Frequency
- Amplitude
- Signal form

It is possible to change the settings while the pump is operating. However, to extend the lifetime it is advisable to turn off the micropump first before changing the settings.



LCD Display

On/Off-switch: Push the on/off switch to turn the pump and control diode on or off.

Save: Push the save switch to save the present settings.

Freq.: Push the switch up to raise and down to reduce the frequency.

Signal: Push the switch once to changes between SRS, rectangular or sine.

Amp.: Push the switch up to raise and down to reduce the amplitude.

To operate the micropump, prepare the controller as described in point 3.7.2 and follow the steps below:

Step 1: Choose a frequency by pushing the frequency switch up and down.

Step 2: Choose one of the signal forms by pushing the signal switch.

Step 3: Set the amplitude by pushing the amplitude switch up and down.

<u>Step 4:</u> Push the on/off switch to turn on the micropump and the control diode.

<u>Step 5:</u> Push the on/off switch again and the micropump will stop while the control diode turns off.

The Save-switch will store the current settings.

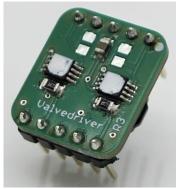


For shutdown of the controller please switch off the mp-Labtronix first and then the main power-switch on the backside of the controller. Then disconnect the power plug. Do not unplug the micropump before switching off the controller.

3.8 mp-valvedriver (T&M)

The valve driver is a small, easy to use driving circuit developed for the Takasago SMV-2R-BN1F shape memory alloy valve as an accessory for the micropumps of the mp6-series.

3.8.1 Pin assignment





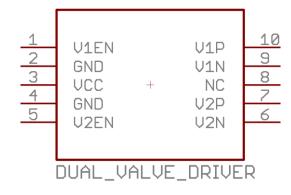


Figure 20 mp-valvedriverT

Figure 21 mp-valvedriverM

Pin	Name	Function	Pin	Name	Function
1	V1EN	Enable pin. Float to enable	10	V1P	Valve 1 output pin+ *
2	GND	Ground	9	V1N	Valve 1 output pin-*
3	VCC	Input supply voltage	8	NC	not connected pin
4	GND	Ground	7	V2P	Valve 2 output pin+ *
5	V2EN	Enable pin. Float to enable	6	V2N	Valve 2 output pin-*

^{*} Note: The Takasago valves do not have polarity. So positive and negative valve output pins can be swapped.

3.8.2 Technical specifications valve driver

Valve driver	Order code: "mp-valve driverT" or "mp-valve driverM"				
The valve driver enables/disables up to two Takasago SMV-2R-BN1F or Memetis BV1101 shape memory alloy					
valves. It is meant to be used in combination with a micro controller. The valve is a normally-closed valve, so it is					
closed when there is no power supplied.					
Dimensions	17,78 x 15,24 x 1,6 mm				
Power supply	4,5 – 5,5 VDC				
Power supply	(5 V recommended for optimized performance)				
Current consumption (one/two va	alves) 70mA/ 140mA (mp-valve driverT)				
VCC = 5V	150mA/ 300mA (mp-valve driverM)				

3.8.3 Electrical Characteristics

Parameter	Symbol	Conditions	Min	Тур.	Max	Unit
Supply voltage	VCC		4.5	5	5.5	V
Standby current		Typical VCC = 5 V		120		μΑ
Input current	IDD	Typical VCC = 5 V		70 ¹ 150 ²		mA
Source current	V1EN V2EN				±100	μΑ
VCC Shutdown supply current		EN = 0 V, VCC = 12 V	2.0	3.7	9	μΑ

¹ mp-valve driverT

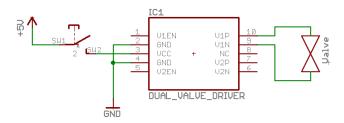
² mp-valve driverM



3.8.4 Examples of circuiting the mp-valvedriver

Manual operation

The first example shows the valves being operated manually by switching the power supply on and off. The valve driver is enabled by default, so the enable pins can be left floating.

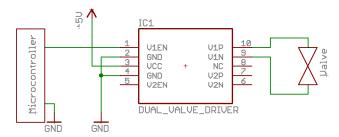


Schematic 22: Manual operation

Operation via microcontroller

The second example shows the valves being operated by a microcontroller. The enable pins are controlled by the micro and the power supply is connected permanently.

To enable the valve set the GPIO pinMode as "Input". To switch the valve of set its pinMode to "Output" with signal "Low".



Schematic 23: Operation via microcontroller

Important Note: If you're using a Multiboard with Firmware "20211102" or older, please update it to the newest version as to not damage the Valvedriver.

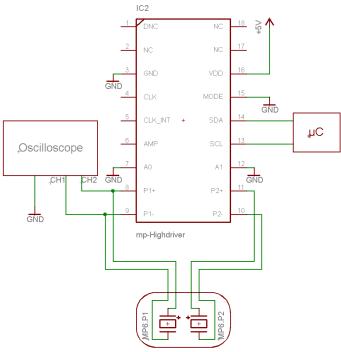


4 Electronics Examination

4.1 Measuring Pump signal

Examining the signal a mp6 micropump receives will need an oscilloscope with at least two channels. Schematic 10 shows where to connect the oscilloscopes probes and connect the GND reference to the circuit.

Attention: Beware of high voltages on the probed wires!



Schematic 10 Example on probing a mp-Highdriver controlled via micro controller by oscilloscope

By setting up a MATH function and substracting channel CH2 of CH1, the actual signal cane be observed that is applied on one piezo actuator oft he mp6 micropump. An example of an output can be seen in Figure 24.

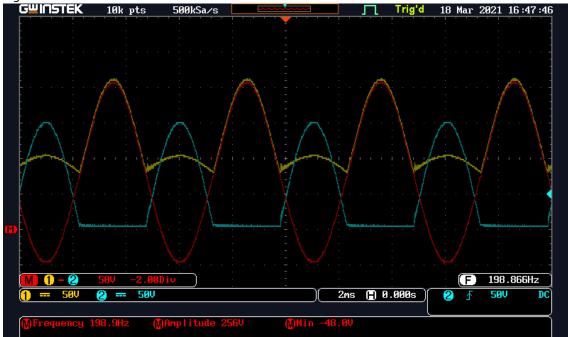


Figure 24 oscilloscope screenshot while probing a Highdriver at 250Vpp & 200Hz with load (mp6) attached



5 Discontinued Products

5.1 Evaluation Board: mp-Multiboard

The mp-Multiboard is available in different combinations and has the following standard components included:

- mp-Multiboard board
- micropump mp6
- Micro-USB cable
- <u>selective</u>: pump driver mp-Lowdriver, mp-Highdriver, mp-Highdriver4



Figure 25 mp-Multiboard

5.1.1 Setup instructions

Make sure pump driver, pump cable harness and pumps are plugged in correctly; install the Arduino Pro Micro USB driver; connect the Multiboard via USB cable to the computer; start the Multiboard App Do not disconnect pumps or driver while Multiboard is powered (via USB or otherwise)!

For more information on the Multiboard App and how to use the Multiboard please have a look at mp6 Software Datasheet available in our download section (https://www.bartels-mikrotechnik.de/downloads).

5.1.2 Electrical Characteristics

The Multiboard is powered via USB connection of the microcontroller. It does comply with the USB standard and does not require more than 500mA to operate under normal conditions. If in any case more current is needed on the board, an external power supply can be connected to the power supply connector labeled "POWER".



Bartels mikrotechnik

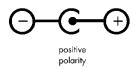


Figure 26: Connector Polarity

The power supply connector is directly wired to the RAW and GND pins of the microcontroller and tied to its onboard voltage regulator. Recommended input voltage for this connector is 7,5–10V. The mate plug should have an outer diameter of 3,5 mm and an inner diameter to be able to accept the 1 mm pin. See Figure 26 for connector polarity. A suited power supply is available in our store.

For all other characteristics of the microcontoller or the pump driver please refere to the corresponding manual or datasheet.

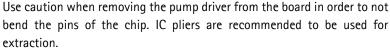
Information about the pump drivers and the mp6 micropumps is available on our download-page: https://www.bartels-mikrotechnik.de/downloads/

5.1.3 Pump drivers

The Multiboard is compatible to our mp-Lowdriver, mp-Highdriver and mp-Highdriver4. Depending on the driver attached to the Multiboard, capabilities and ranges of amplitude and frequency can change. Please refer to the according manual for details. When attaching the pump driver make sure to follow the correct orientation as shown in the pictures below (notice the white dot always pointing outwards). Important note: Only one driver can be connected to the board at a time. single and quad drivers have one pump channel in common and can damage each other.











5.1.4 Valve driver

The socket labeled "Valve driver" accepts our current dual valve driver that is able to drive two Takasago valves. The valves are current driven and the driver takes care of generating and supplying a constant current on two separate outputs on demand. The two outputs are digitally controlled via the Multiboard.

Attention: Since the valve driver needs a minimum of 4.5V to operate correctly, an external power supply needs to be attached to the Multiboard. The 5V rail of the Multiboard supplied by the USB is not reliable enough, to guarantee operation. See chapter

Electrical Characteristics for more details.



5.1.5 Sensors

Since App version 1.5 (and Firmware 20210428) the Multiboard supports readout of digital flow and pressure sensors. The liquid flow-sensors of type SLF3S-0600F and SLF3S-1300F from the manufacturer Sensirion can be attached to the connector labeled "Flow" via a straight 6-pin ribbon cable. Both sensors can be bought form our store with the ribbon cable included. For measuring pressure, we have implemented the readout for Honeywell ABP-series 15PSI digital pressure sensor. This one is sold on our store with a cable and 5-pin connector already attached, fitting the pinheader labeled "Pressure digital".

5.1.6 Auxiliary connectors



Figure 27 mp-Multiboard Connectors

The Multiboard has multiple auxiliary connectors available. Some of these are not in use at the moment and will be documented in this manual once fully implemented. Nevertheless, here is a list of the connectors and their (future) use:

1.1 & 1.2	CH2 / CH1, CH3 / CH4	Two 8-pin connectors for pump cable harness. Each cable harness has two FFC connectors on the other end to connect to the micropump flex cable
2	Mp-Highdriver4	Mp-Highdriver4 Socket
3	Mp-Highdriver- / mp-	Mp-Highdriver- / mp-Lowdriver Socket
	Lowdriver	
4	Mp-Valvedriver	Mp-Valvedriver Socket
5	Valve Plug	5-pin connector for two active valves
6	Flow	6-pin connector for an I ² C flow-sensor
7	Pressure digital	5-pin connector for an I ² C pressure-sensor
8	Pressure analog	3-pin connector for an analog pressure-sensor
9	Power	Barrel power supply connector
10	Micro Controller	Arduino Pro Micro with Multiboard Firmware
11	TP1	12-pin header for custom purposes



5.1.7 Package Dimensions

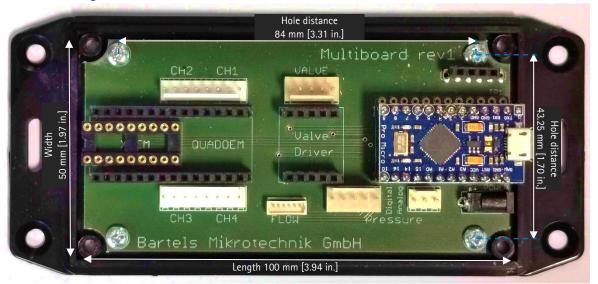


Figure 28: Package Dimensions



6 Important Notices

This operating manual contains necessary instructions for the installation, commissioning, operation and maintenance of the mp6-series. The manual is intended to help you achieving optimal results in a short time and shall also assist avoiding possible sources of errors. The operating manual of the controllers and the accessories are available separately.

The products have been designed with state-of-the-art technology and in accordance with all relevant safety regulations. However, a risk of damage to the units, other property, the operator and/or other persons cannot be fully excluded.

Always ensure that specialized and trained personnel will comply with the following general instructions. Therefore, please keep this manual and hand out copies as required.

All values are approximate and no guarantee of specific technical properties. Changes in the course of technical progress are possible without notice.

6.1 Warranty

The mp6 micropumps have been developed for the transport of gases or liquids. The controllers have been developed for operating the mp6 micropumps. Bartels Mikrotechnik can assume no liability for damages resulting from the pump media. This applies especially for hazardous fluids.

The mp6 micropumps must be operated with Bartels Mikrotechnik electronics. Bartels Mikrotechnik GmbH cannot guarantee the proper work of the units with customer specific electronics. If other controllers than the ones from Bartels Mikrotechnik are used, Bartels Mikrotechnik disclaims any warranty.

Bartels Mikrotechnik assumes no liability for abnormal handling, improper or negligent use of the mp6 micropumps and the controller that is not conform to the specified purpose of the system. This applies especially for micropump controllers, components and systems of other manufacturers, which have not been certified by Bartels Mikrotechnik.

We guarantee that the mp6 micropumps comply with the actual state of scientific and technical knowledge and due to this, the operational risks are limited to a minimum.

Bartels Mikrotechnik GmbH warrants solely to the original purchaser of this product for a period of 12 months (one year) from the date of delivery that this product shall be of the quality, material and workmanship defined in Bartels Mikrotechnik GmbH published specifications of the product. Within such period, if proven to be defective, Bartels Mikrotechnik GmbH shall repair and/or replace this product, in Bartels Mikrotechnik GmbH's discretion, free of charge to the Buyer, provided that:

- notice in writing describing the defects shall be given to Bartels Mikrotechnik GmbH within fourteen (14) days after their appearance
- such defects shall be found, to Bartels Mikrotechnik GmbH's reasonable satisfaction, to have arisen from Bartels Mikrotechnik GmbH's faulty design, material or workmanship
- the defective product shall be returned to Bartels Mikrotechnik's factory at the Buyer's expense
- the warranty period for any repaired or replaced product shall be limited to the unexpired portion of the original period.

This warranty does not apply to any equipment which has not been installed and used within the specifications recommended by Bartels Mikrotechnik GmbH for the intended and proper use of the equipment.



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6.2 Warning, Personal Injury

Bartels Mikrotechnik GmbH rejects any responsibility for damages to persons or property resulting from non-compliance with the instructions in this manual. In this case all warranties shall be void.

Do not use this product as safety or emergency stop devices or in any other application where failure of the product could result in personal injury. Do not use this product for applications other than its intended and authorized use. Before installing, handling, using or servicing this product, please consult the data sheet and application notes. Failure to comply with these instructions could result in death or serious injury. If the Buyer shall purchase or use Bartels Mikrotechnik GmbH products for any unintended or unauthorized application, Buyer shall defend, indemnify and hold harmless Bartels Mikrotechnik GmbH and its officers, employees, subsidiaries, affiliates and distributors against all claims, costs, damages and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if Bartels Mikrotechnik GmbH shall be allegedly negligent with respect to the design or the manufacture of the product.

Moreover, please note that components of the controller and pump are operating with high voltage. Therefore, persons wearing pacemakers are recommended to avoid the operating system. Do not open the housing of the micropump and the controllers.

The use of liquids, which may alone or in combination create explosive or otherwise health-endangering conditions (including vapors) is not permitted.

All work in connection with the installation, assembly, commissioning/decommissioning, disassembly, operation, servicing, cleaning and repairing of the pump and the controller must be carried out by qualified, suitably trained and instructed personnel. Work on electrical components and assemblies must be carried out by personnel with the necessary qualifications and skills.

We guarantee that the micropumps comply with the actual state of scientific and technical knowledge and due to this, the operational risks are limited to a minimum.

ESD Precautions: The inherent design of the active electronic components, i.e. mp-Lowdriver, -Highdriver, -Highdriver4, Valvedriver and -Labtronix causes it to be sensitive to electrostatic discharge (ESD). To prevent ESD-induced damage and/or degradation, take customary and statutory ESD precautions when handling this product.



6.3 Declaration of conformity

Bartels Mikrotechnik GmbH declares that the products are compliant to the RoHS directive 2011/65/EU. The controllers comply with the requirements of EMV 2014/30/EU and CE markings have been affixed to the devices. Additionally, the controllers are also compliant to the EU Low Voltage Directive 2014/35/EU.

7 Company information



Bartels Mikrotechnik is a globally active manufacturer and development service provider in the field of microfluidics. In the microEngineering division, the company supports industrial customers in the modification, adaptation and new development of high-performance and market-oriented product solutions through the innovative means of microsystems technology. The second division, microComponents, produces and distributes microfluidic products and systems, especially for miniaturized and portable applications. Our key products are micropumps that convey smallest quantities of gases or liquids and are used in a variety of ways in biotechnology, pharmaceuticals, medical technology and numerous other applications.

Bartels Mikrotechnik with passion for microfluidics!

Contact us:

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